SPECIFICATION FOR APPROVAL

(♦)	Preliminary Specification
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() Final Specification

Customer	Quanta	
MODEL		

SUPPLIER	LG.Philips LCD Co., Ltd.	
*MODEL	LP154WX4	
Suffix	TLC1	

^{*}When you obtain standard approval, please use the above model name without suffix

SIGNATURE
our confirmation with

APPROVED BY	SIGNATURE
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Ver. 0.0 Apr. 13, 2007 1 / 27



Contents

No	ITEM			
	COVER	1		
	CONTENTS	2		
	RECORD OF REVISIONS	3		
1	GENERAL DESCRIPTION	4		
2	ABSOLUTE MAXIMUM RATINGS	5		
3	ELECTRICAL SPECIFICATIONS			
3-1	ELECTRICAL CHARACTREISTICS	6		
3-2	INTERFACE CONNECTIONS	7		
3-3	SIGNAL TIMING SPECIFICATIONS	9		
3-4	SIGNAL TIMING WAVEFORMS	9		
3-5	COLOR INPUT DATA REFERNECE			
3-6	POWER SEQUENCE	11		
4	OPTICAL SFECIFICATIONS			
5	MECHANICAL CHARACTERISTICS			
6	RELIABLITY			
7	INTERNATIONAL STANDARDS			
7-1	SAFETY	21		
7-2	EMC	21		
8	PACKING			
8-1	DESIGNATION OF LOT MARK	22		
8-2	PACKING FORM	22		
9	PRECAUTIONS	23		
Α	APPENDIX. Enhanced Extended Display Identification Data	25		



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Apr. 13. 2007	-	First Draft (Preliminary Specification)	0.0

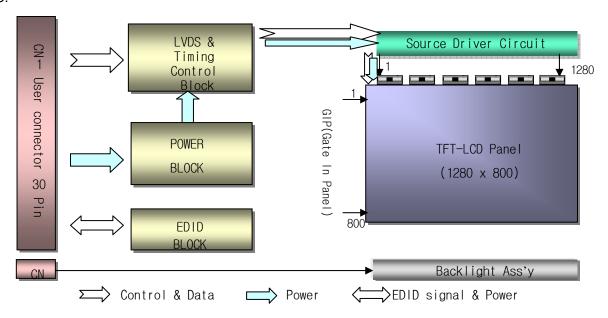


1. General Description

The LP154WX4 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.4 inches diagonally measured active display area with WXGA resolution(800 vertical by 1280 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP154WX4 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP154WX4 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP154WX4 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.4 inches diagonal		
Outline Dimension	344.0(H, typ) × 222.0(V, typ) × 6.2(D, typ) [mm]		
Pixel Pitch	0.25875mm × 0.25875 mm		
Pixel Format	1280 horiz. By 800 vert. Pixels RGB strip arrangement		
Color Depth	6-bit, 262,144 colors		
Luminance, White	200 cd/m ² (Typ.5 point)		
Power Consumption	Total 5.6 Watt(Typ.) @ LCM circuit 1.4Watt(Typ.), B/L input 4.2Watt(Typ.)		
Weight	575 g (Max.), 560g(Typ.)		
Display Operating Mode	Transmissive mode, normally white		
Surface Treatment	Glare treatment of the front polarizer		
RoHS Comply	Yes		

Ver. 0.0 Apr. 13, 2007 4 / 27



2. Absolute Maximum Ratings

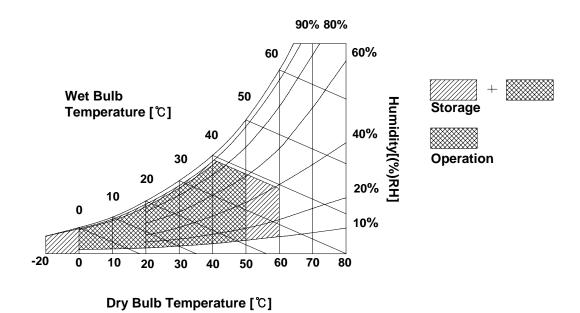
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes	
Farameter	Syllibol	Min	Max	Office	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 0.0 Apr. 13, 2007 5 / 27



3. Electrical Specifications

3-1. Electrical Characteristics

The LP154WX4 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Values Parameter Symbol Unit Notes Min Тур Max MODULE: VCC Power Supply Input Voltage 3.0 3.3 3.6 V_{DC} Power Supply Input Current 460 400 mΑ 340 I_{CC} Power Consumption Рс 1.4 1.6 Watt Differential Impedance Ohm Zm 90 100 110 LAMP: Operating Voltage V_{BL} 665(6.8mA) 690(6.0mA) 830(3.0mA) V_{RMS} Operating Current 3.0 6.0 6.8 mA_{RMS} I_{BL} 4.2 **Power Consumption** P_{BL} 4.6 Operating Frequency 60 80 45 kHz f_{BL} Discharge Stabilization Time 3 Min Ts Life Time 12,000 Hrs Established Starting Voltage at 25℃ Vs 1200 V_{RMS} at 0 ℃ 1500 V_{RMS}

Table 2. ELECTRICAL CHARACTERISTICS

Note)

- 1. The specified current and power consumption are under the Vcc = 3.3V, $25^{\circ}C$, fv = 60Hz condition whereas full black pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics.
- 4. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
- 5. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 6. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.
 Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 7. It is defined the brightness of the lamp after being lighted for 5 minutes as 100%.

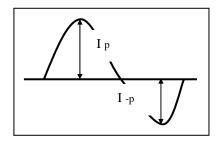
 T_s is the time required for the brightness of the center of the lamp to be not less than 95%.
- 8. The lamp power consumption shown above does not include loss of external inverter. The applied lamp current is a typical one.

Ver. 0.0 Apr. 13, 2007 6 / 27



Note)

- Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
 It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2 \pm 10\%}$.
 - * Inverter output waveform had better be more similar to ideal sine wave.



Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

Ver. 0.0 Apr. 13, 2007 7 / 27



3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model GT101-30S-HR11 manufactured by LSC.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	1, Interface chips
4	V EEDID	DDC 3.3V power	1.1 LCD: SW, SW0604_M (LCD Controller)
5	NC	Reserved for supplier test point	including LVDS Receiver
6	Clk EEDID	DDC Clock	1.2 System : ? or equivalent * Pin to Pin compatible with LVDS
7	DATA EEDID	DDC Data	Till to Till companie with EVBC
8	R _{IN} 0-	Negative LVDS differential data input	2. Connector
9	R _{IN} 0+	Positive LVDS differential data input	2.1 LCD :IS100-C30R-C15 ,UJU Elec. GT101-30S-HR11,LS Cable
10	GND	Ground	its compatibles
11	R _{IN} 1-	Negative LVDS differential data input	2.2 Mating: FI-X30M or equivalent.
12	R _{IN} 1+	Positive LVDS differential data input	2.3 Connector pin arrangement
13	GND	Ground	
14	R _{IN} 2-	Negative LVDS differential data input	30 1
15	R _{IN} 2+	Positive LVDS differential data input	
16	GND	Ground	
17	CLKIN-	Negative LVDS differential clock input	II CD Madula Daar Viewi
18	CLKIN+	Positive LVDS differential clock input	[LCD Module Rear View]
19	GND	Ground	
20	NC	No Connect	
21	NC	No Connect	
22	GND	Ground	
23	NC	No Connect	
24	NC	No Connect	
25	GND	Ground	
26	NC	No Connect	
27	NC	No Connect	
28	GND	Ground	
29	NC	No Connect	
30	NC	No Connect	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible. The mating connector part number is AMP1674817-2 or equivalent.

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored Pink and the low voltage side terminal is White.

Condition: VCC =3.3V

9/27



Product Specification

3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	66.0	71.0	76.0	MHz	
Hsync	Period	Thp	1360	1440	1480		
	Width	t _{wH}	16	32	48	tCLK	
	Width-Active	t _{WHA}	1280	1280	1280		
Vsync	Period	t _{VP}	809	823	860		
	Width	t _{wv}	2	6	10	tHP	
	Width-Active	t _{wva}	800	800	800		
Data	Horizontal back porch	t _{HBP}	40	80	96	+CL V	
Enable	Horizontal front porch	t _{HFP}	24	48	56	tCLK	
	Vertical back porch	t _{VBP}	6	14	32	+I ID	
	Vertical front porch	t _{VFP}	1	3	18	tHP	

3-4. Signal Timing Waveforms

Ver. 0.0

High: 0.7VCC Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc **DCLK** t_{HP} Hsync **t**WHA t_{HFP} t_{HBP} Data Enable t_{VP} Vsync t_{VFP} twva t_{VBP} Data Enable

Apr. 13, 2007



3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	ut Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
`	30101	MSI	3				LSB	MSE	3				LSB		3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	В0
	Black	0	0			0	0	0	0	0	0	0	0	0	0	0		0	0
	Red	1	1			1	1	0	0	0	0	0	0	0	0	0		0	0
	Green	0	0	0		0	0	1	1	. 1			1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	. 1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN		ļ			 														
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	····· 1
BLUE		·····			 			ļ			 								
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	 1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	 1	1
	1 (/																		



3-6. Power Sequence

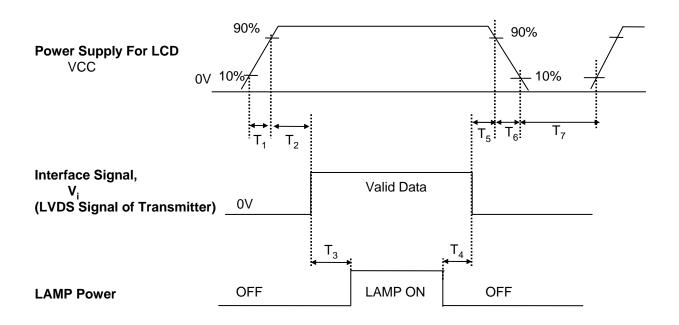


Table 8. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	0.5	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	0	-	10	(ms)
T ₇	200	-	-	(ms)

Note)

- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

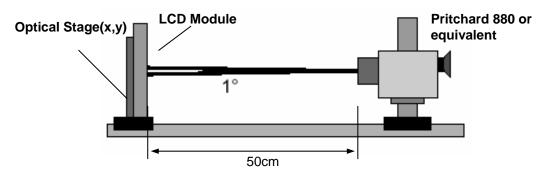


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 71.0MHz, I_{BL} = 6.0mA

Dorometer	Cumbal		Values		Linita	Notes
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	400	600	-		1
Surface Luminance, white	L _{WH}	170	200	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6		3
Response Time	Tr _R + Tr _D		16		ms	4
Color Coordinates						
RED	RX	0.570	0.600	0.630	[
	RY	0.321	0.351	0.381		
GREEN	GX	0.295	0.325	0.355		
	GY	0.524	0.554	0.584		
BLUE	BX	0.124	0.154	0.184		
	BY	0.115	0.145	0.175		
WHITE	WX	0.283	0.313	0.343		
[WY	0.299	0.329	0.359]	
Viewing Angle]	5
x axis, right(Φ=0°)	Θr	40	45	-	degree	
x axis, left (Φ=180°)	Θl	40	45	-	degree	
y axis, up (⊕=90°)	Θu	10	15	-	degree	
y axis, down (Φ=270°)	Θd	30	35	-	degree	
Gray Scale						6

Ver. 0.0 Apr. 13, 2007 12 / 27



Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{ WHITE}} = \frac{\text{Maximum}(\mathsf{L}_{1}, \mathsf{L}_{2}, \ \dots \ \mathsf{L}_{13})}{\text{Minimum}(\mathsf{L}_{1}, \mathsf{L}_{2}, \ \dots \ \mathsf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_V = 60Hz$$

Gray Level	Luminance [%] (Typ)
LO	0
L7	0.80
L15	4.25
L23	10.9
L31	21.0
L39	34.8
L47	52.5
L55	74.2
L63	100

Ver. 0.0 Apr. 13, 2007 13 / 27



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

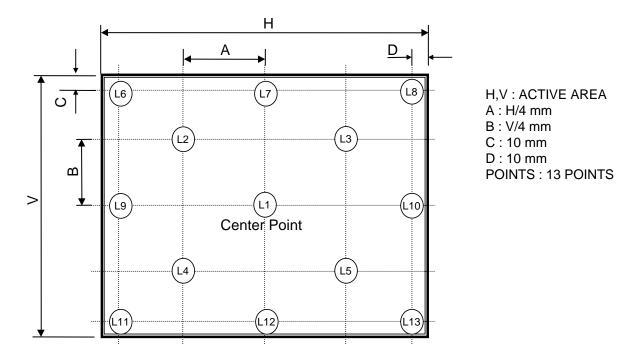
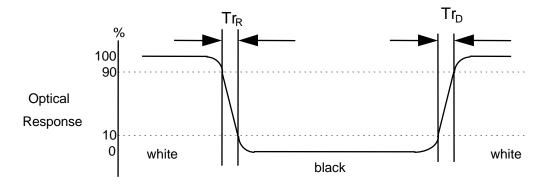


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

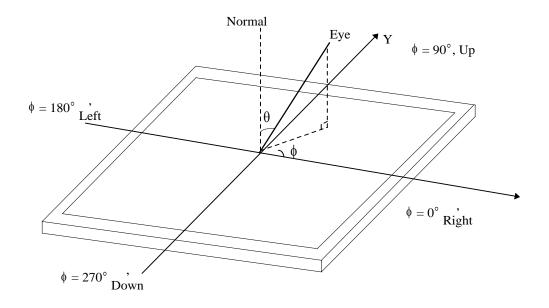


Ver. 0.0 Apr. 13, 2007 14 / 27



FIG. 4 Viewing angle

<Dimension of viewing angle range>



Ver. 0.0 Apr. 13, 2007 15 / 27



5. Mechanical Characteristics

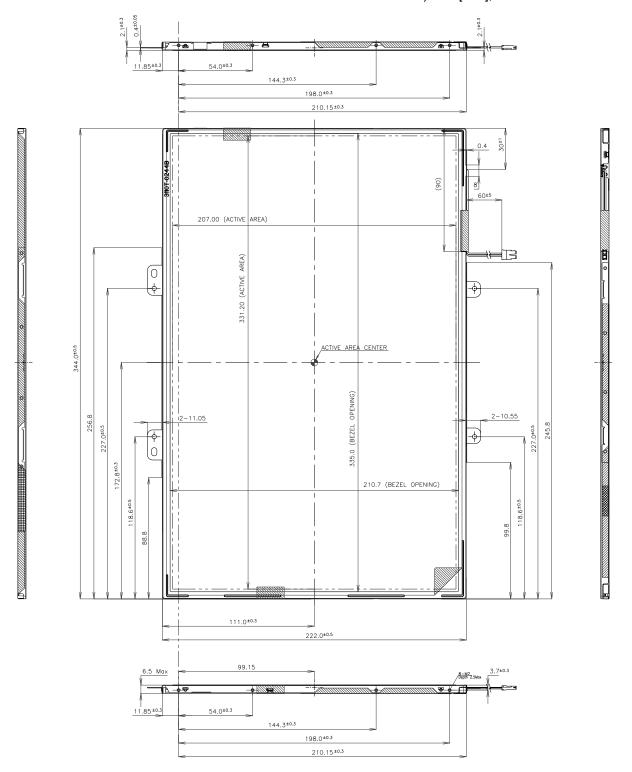
The contents provide general mechanical characteristics for the model LP154WX4. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	344.0 ± 0.5mm
Outline Dimension	Vertical	222.0 ± 0.5mm
	Thickness	6.5mm (max)
Bezel Area	Horizontal	335.0 ± 0.5 mm
Dezei Alea	Vertical	210.7 ± 0.5mm
Active Diepley Area	Horizontal	331.2 mm
Active Display Area	Vertical	207.0 mm
Weight	560g(Typ.), 575g (Max.)	
Surface Treatment	Glare treatment of the front pola	rizer



<FRONT VIEW>

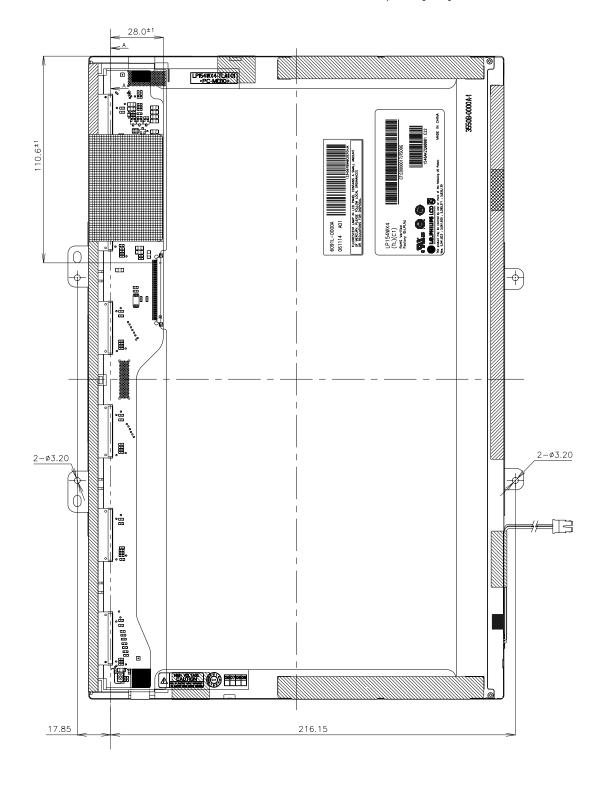
Note) Unit:[mm], General tolerance: \pm 0.5mm





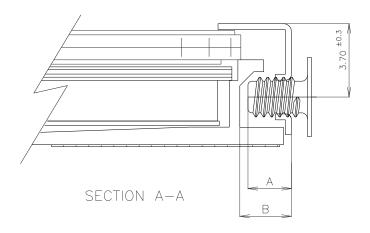
<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



- * Mounting Screw Length (A) = 2.0(Min) / 2.5(Max)
- * Mounting Screw Hole Depth (B) = 2.5(Min)
- * Mounting hole location: 3.7(typ.)
- * Torque : 2.5 kgf.cm(Max)

(Measurement gauge: torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.



6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

Ver. 0.0 Apr. 13, 2007 20 / 27



7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J K L	А	В	С	D	Е	F	G	Н	I	J	К	L	М
---	---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH) D : YEAR

E: MONTH F ~ M: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 20 pcs

b) Box Size: 441mm × 373mm × 348mm

Ver. 0.0 Apr. 13, 2007 22 / 27



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 0.0 Apr. 13, 2007 23 / 27



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3 LP154WX4−TLC1 E−EDID DATA (ver0.0) 2007,04,13

Byte#	Byte#	-	Val	ue	Value	
(decimal)		Field Name and Comments	(HE	X)	(binary)	
0	00	Header	0	0	0000 0000	
1	01				1111 1111	
2	02		F	F	1111 1111	
3	03				1111 1111	Header
4	04		F	F	1111 1111	
5	05		F	F	1111 1111	
- 6	06				1111 1111	
7	07				0000 0000	
8	08	EISA manufacturer code = LPL	3			
9	09		-	С		
10	0A	Product code = 00DB	0	0	0000 0000	
11	OB	(Hex, LSB first)			1101 1011	
12	OC.	32-bit serial number	0	0	0000 0000	Vender/
13	0D		0	0	0000 0000	Product ID
14	0E		0	0	0000 0000	
15	0F					
16	10	Week of manufacture	0	0	0000 0000	
17	11	Year of manufacture = 2006		0		
18	12	EDID Structure version # = 1	0	1		EDID Version/
19	13	EDID Revision # = 3	0	3	0000 0011	Revision
20	14	Video input definition = Digital I/p,non TMDS CRGB			1000 0000	
21	15	Max H image size(εm) = 33,12εm(33)			0010 0001	Display
22	16	Max V image size(cm) = 20,7cm(21)	1	5	0001 0101	Parameter
23	17	Display gamma = 220			0111 1000	
24	18	Feature support(DPMS) = Active off, RGB Color			0000 1010	
25	19	Red/Green low Bits	_	3	1011 0011	
26	1A	Blue/White Low Bits			0100 0000	
27	1B	Red X Rx = 0,600			1001 1001	
28	1C	Red Y Ry = 0,351			0101 1001	
29	1D	Green X Gx = 0,325		3.		Color
30	1E	Green Y Gy = 0,554				Characteristic
31	1F	Blue X Bx = 0,154			0010 0111	
32	20	Blue Y By = 0,145	2 5		0010 0101	
33 34	21	White X Wx = 0,313			• • • • • • • • • • • • • • • • • • • •	
35	22 23	White Y Wy = 0,329	0	4		Established
36	24	Established Timing I Established Timing II			0000 0000	Timings
$\overline{}$					0000 0000	Timings
37	25	Manufacturer's Timings	-	_		
38 39	26 27	Standard Timing Identification 1 was not used	-	1	0000 0001	
40	28	Standard Timing Identification 1 was not used Standard Timing Identification 2 was not used	-	1	0000 0001	
$\overline{}$		Standard Timing Identification 2 was not used Standard Timing Identification 2 was not used	\rightarrow	-	0000 0001	
41	29	ů.	$\overline{}$	1	0000 0001	
42	2A	Standard Timing Identification 3 was not used	_	1	0000 0001	
43	2B	Standard Timing Identification 3 was not used	-	1	0000 0001	
44	2C	Standard Timing Identification 4 was not used	-	1	0000 0001	Standard
45	2D	Standard Timing Identification 4 was not used	-	1	0000 0001	Timing ID
46	2E	Standard Timing Identification 5 was not used	0	1	0000 0001	
47	2F	Standard Timing Identification 5 was not used	\rightarrow	1	0000 0001	
48	30	Standard Timing Identification 6 was not used	-	1	0000 0001	
49	31	Standard Timing Identification 6 was not used	\rightarrow	1	0000 0001	
50	32	Standard Timing Identification 7 was not used	-	1	0000 0001	
51	33	Standard Timing Identification 7 was not used	0	1	0000 0001	
52	34	Standard Timing Identification 8 was not used	0	1	0000 0001	
53	35	Standard Timing Identification 8 was not used	0	1	0000 0001	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

S4	Byte#	Byte#	Field Name and Comments	Value		
SS 37 Cstored LSB first)	(decimal)	(HEX)	4000 1/4 000 10 000	(HEX)		
Second S			7	BIC	1011 1100	
Second			(Stored LSB first)	1 B	0001 1011	
Second			Horizontal Active = 1280 pixels			
S9			Horizontal blanking = 160 pixels			
SO			• • • • • • • • • • • • • • • • • • • •			
Signature State						
SE2 3E				L		
63 3F						1
Set	62					Descriptor
65			Horizontal Sync Pulse Width = 32 pixels			#1
66						
67						
68						
69	67	43	Vertical Image Size = 207,0mm(207)			
70	68	44	Horizontal & Vertical Image Size	1 0	0001 0000	
71	69	45				
71	70	46	Vertical Border = 0	0 0	0000 0000	
72 48 Detailed Timing Descriptor #2 0 0 000 0000 0 000 0000 0 000 0000 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 000 0000 0 0 0000 0000 0 0 00	71	47	Non-Interlaced,Normal display,no stereo,Digital separate sync,H/V poi negatives	1 9	0001 1001	
74	72	48	Detailed Timing Descriptor #2			
75	73	49	-			
76		4A				
77						
T8		4C				
79						
S0 S0 S0 S0 S0 S0 S0 S0						
S1 S1						
S2 S2						-
83 53						#2
84						
85						
S6						
87 57						
88		57				
89						
90						
SB			Detailed Timing Descriptor #3	0 0	0000 0000	
95 5F L 4 C 0100 1100 96 60 G 4 7 0100 0111 97 61 P 5 0 0101 0000 98 62 h 6 8 0110 1000 99 63 i 6 9 0110 1001 100 64 I 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 P 7 0 0111 0000 103 67 S 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100				0.00	0000 0000	
95 5F L 4 C 0100 1100 96 60 G 4 7 0100 0111 97 61 P 5 0 0101 0000 98 62 h 6 8 0110 1000 99 63 i 6 9 0110 1001 100 64 I 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 P 7 0 0111 0000 103 67 S 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100	92	5C		0 0	0000 0000	
95 5F L 4 C 0100 1100 96 60 G 4 7 0100 0111 97 61 P 5 0 0101 0000 98 62 h 6 8 0110 1000 99 63 i 6 9 0110 1001 100 64 I 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 P 7 0 0111 0000 103 67 S 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100				FE	1111 1110	
95 5F L 4 C 0100 1100 96 60 G 4 7 0100 0111 97 61 P 5 0 0101 0000 98 62 h 6 8 0110 1000 99 63 i 6 9 0110 1001 100 64 I 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 P 7 0 0111 0000 103 67 S 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100	94	5E		0 0	0000 0000	
97 61 P 5 0 0101 0000 Timing 98 62 h 6 8 0110 1000 Description 99 63 i 6 9 0110 1001 100 64 I 6 0 0110 1100 101 65 i 6 9 0110 1001 102 66 p 7 0 0111 0000 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100			L	4 C	0100 1100	
97 61 P 5 0 0101 0000 Timing 98 62 h 6 8 0110 1000 Description 99 63 i 6 9 0110 1001 100 64 I 6 0 0110 1100 101 65 i 6 9 0110 1001 102 66 p 7 0 0111 0000 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100			G 			
99 63 i 6 9 0110 1001 100 64 i 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 p 7 0 0111 0000 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100			P	w		
100 64 I 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 p 7 0 0111 0000 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100			<u> </u>			-
101 65 i 6 9 0110 1001 102 66 p 7 0 0111 0000 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100			1			#3
102 66 p 7 0 0111 0000 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100			<u> </u>			
103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100				w		
104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100				w		
105 69 C 4 3 0100 0011 106 6A D 4 4 0100 0100			ì	w		
106 6A D (4 4 0100 0100			T			
			T D	w		
	107	6B	LF	ÖTÄ	0000 1010	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

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Byte# (decima	Byte#	Field Name and Comments		Va.	lue 'Y'	71.1	ue arv)		
108	6C	Detailed Timing Descriptor #4		0	~	0000	3.7		-
109	6D	Detailed Tilling Descriptor #4	•••••		~ ~	0000			- 1
110			•••••		~ ~	0000			- 1
111			•••••		Ē	1111	1110		- 1
112	70		•••••		7	0000	0000		- 1
113				4	<u>-</u>	0100			- 1
114	72				ř		0000	Detailed	- 1
115	73			3	"		00001	Timing	- 1
116	74	<u> </u>		3	r <u>i</u>		0101	Description	.
117		, , , , , , , , , , , , , , , , , , ,			7		0101	Description	"Ι
									11
3iiii 7.5 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -									
								~~ <u>V</u>	
			m4 ;	"" "" <u>"</u> W	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	.00. hi(1
70	K:::::: 	1	3	n ruo	1- [(D00000			125
. 7E		sion ftag = (10	(0, 1	0 000	XEI: ((10010 \$ 2	xtens	sion Flag	128
· 7F	Chec	ksum	4 (8 200	10. (0110	Che	cksum	127
									_

Ver. 0.0 Apr. 13, 2007 27 / 27