

AN8839NSBE2 IC701 SERVO AMP

AN8739SBE2 IC703 FOCUS COIL / TRACKING COIL / TRAVERSE MOTOR / SPINDLE MOTOR DRIVE

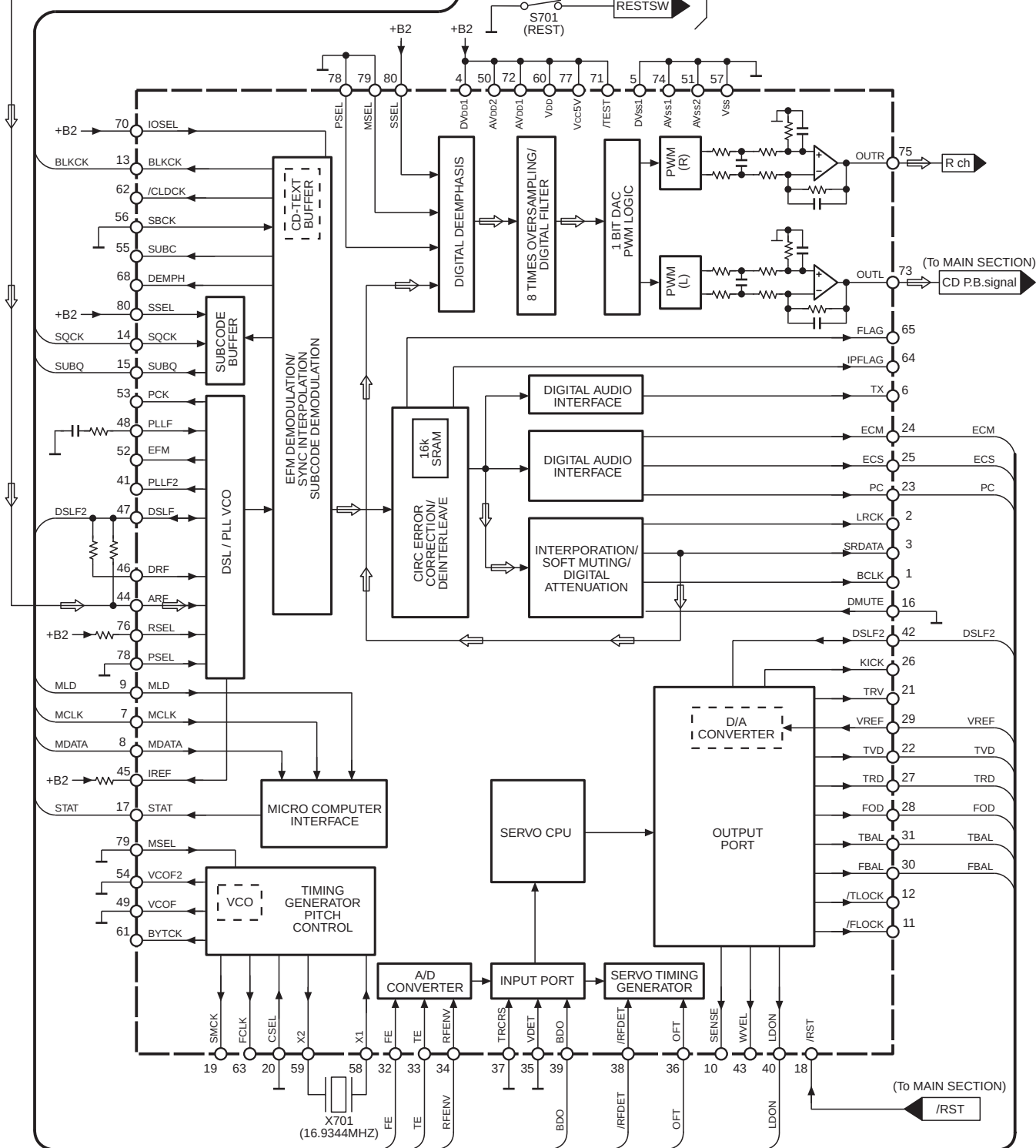
The diagram illustrates the internal circuitry of the AN8839NSBE2 IC701 Servo Amp and the AN8739SBE2 IC703 Focus Coil / Tracking Coil / Traverse Motor / Spindle Motor Drive. The IC701 section includes a laser power drive, photo detector, and various signal processing blocks like VCBA, SUBTRACTOR, and DETECTORS. The IC703 section shows the external connections for power supply rails (+B1, +B2, +B8), ground (GND), and various input/output pins for motors and coils.

AN8739SBE2
IC703
FOCUS COIL / TRACKING
COIL / TRAVERSE MOTOR /
SPINDLE MOTOR DRIVE

MN662790RSC
IC702
SERVO PROCESSOR / DIGITAL SIGNAL PROCESSOR
DIGITAL FILTER / D/A CONVERTER

NOTE • ➡ : CD PLAYBACK SIGNAL LINE

(To MAIN SECTION)



The diagram illustrates a complex radio receiver circuit, primarily a PLL FM/AM radio. It is organized into two main functional blocks: the FM FRONT END and the PLL FREQUENCY SYNTHESIZER.

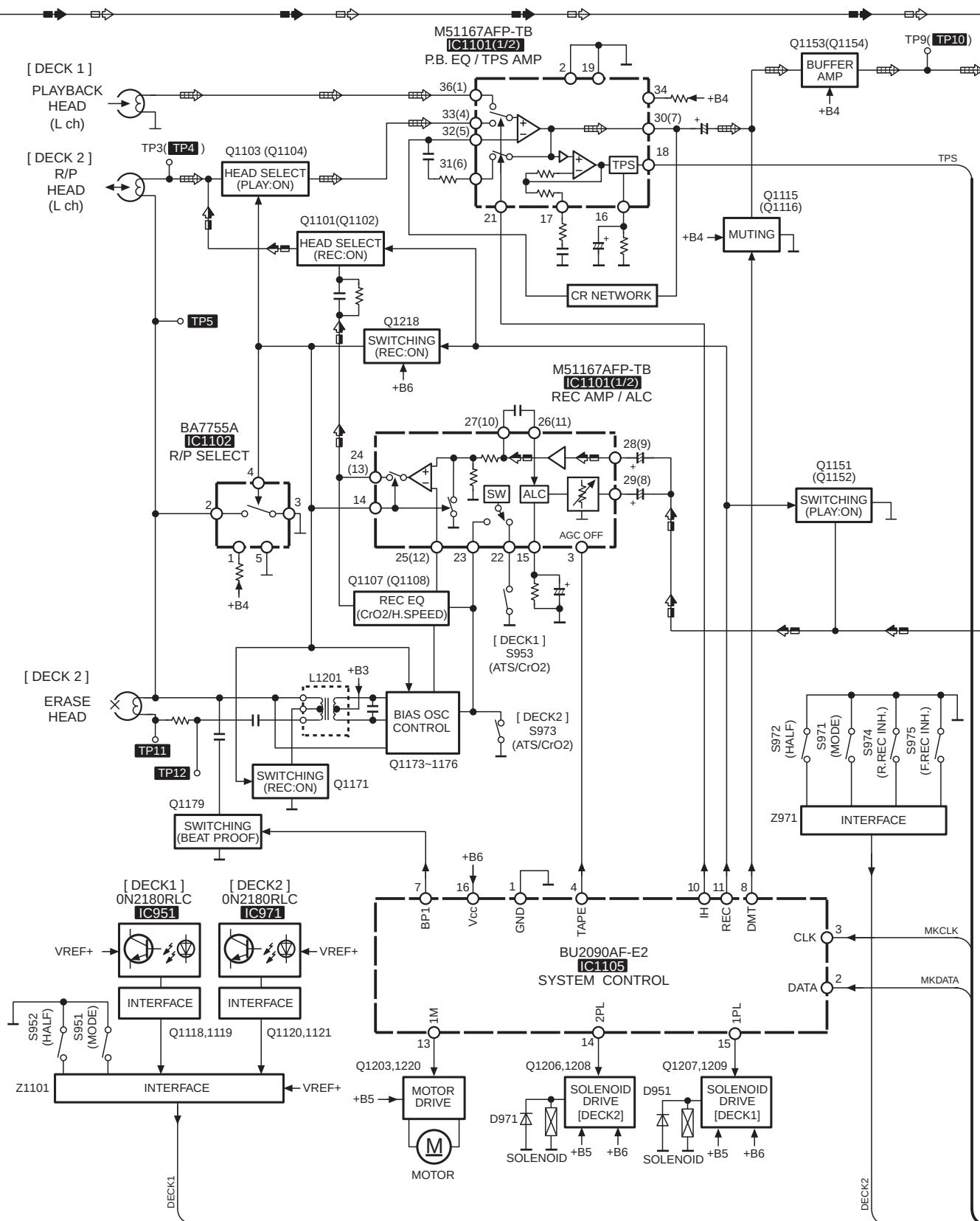
FM FRONT END (Top Section):

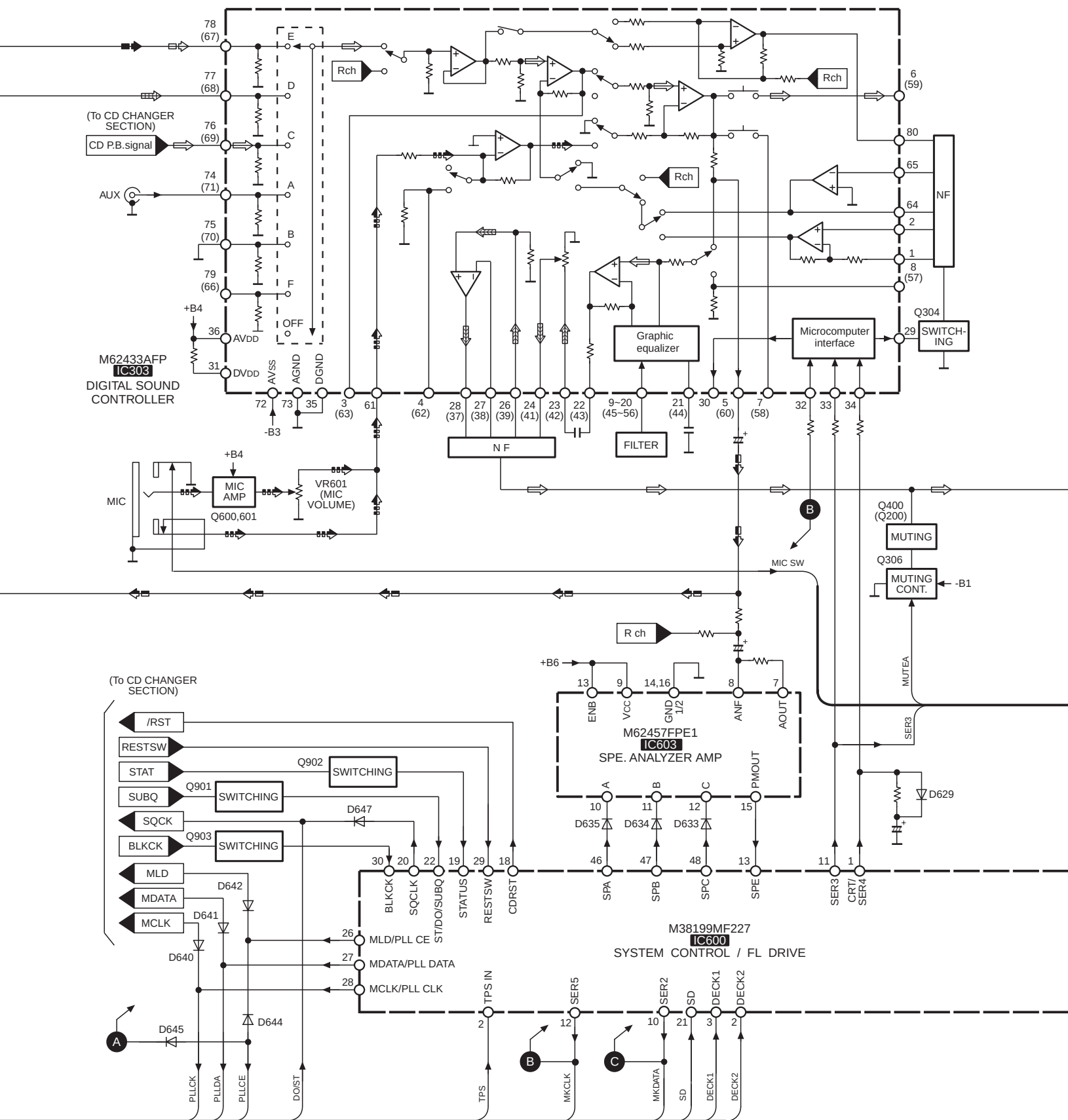
- Inputs:** FM ANT (FM Antenna) and AM ANT (AM Antenna).
- FM Path:** Includes an RF AMP (Q1A, D1A), FM MIXER (Q2), LOCAL OSC (Q3, D3), BUFFER AMP (Q4), and FM IF AMP (Q101). It also features a LOCAL OSC and a BUFFER AMP.
- AM Path:** Includes an AM ANT COIL, AM OSC COIL, and a Z101 transformer.
- Filters:** CF201 and CF202 are used for frequency filtering.
- Power:** The circuit is powered by +B4 (FM) and +B3 (FM:ON).

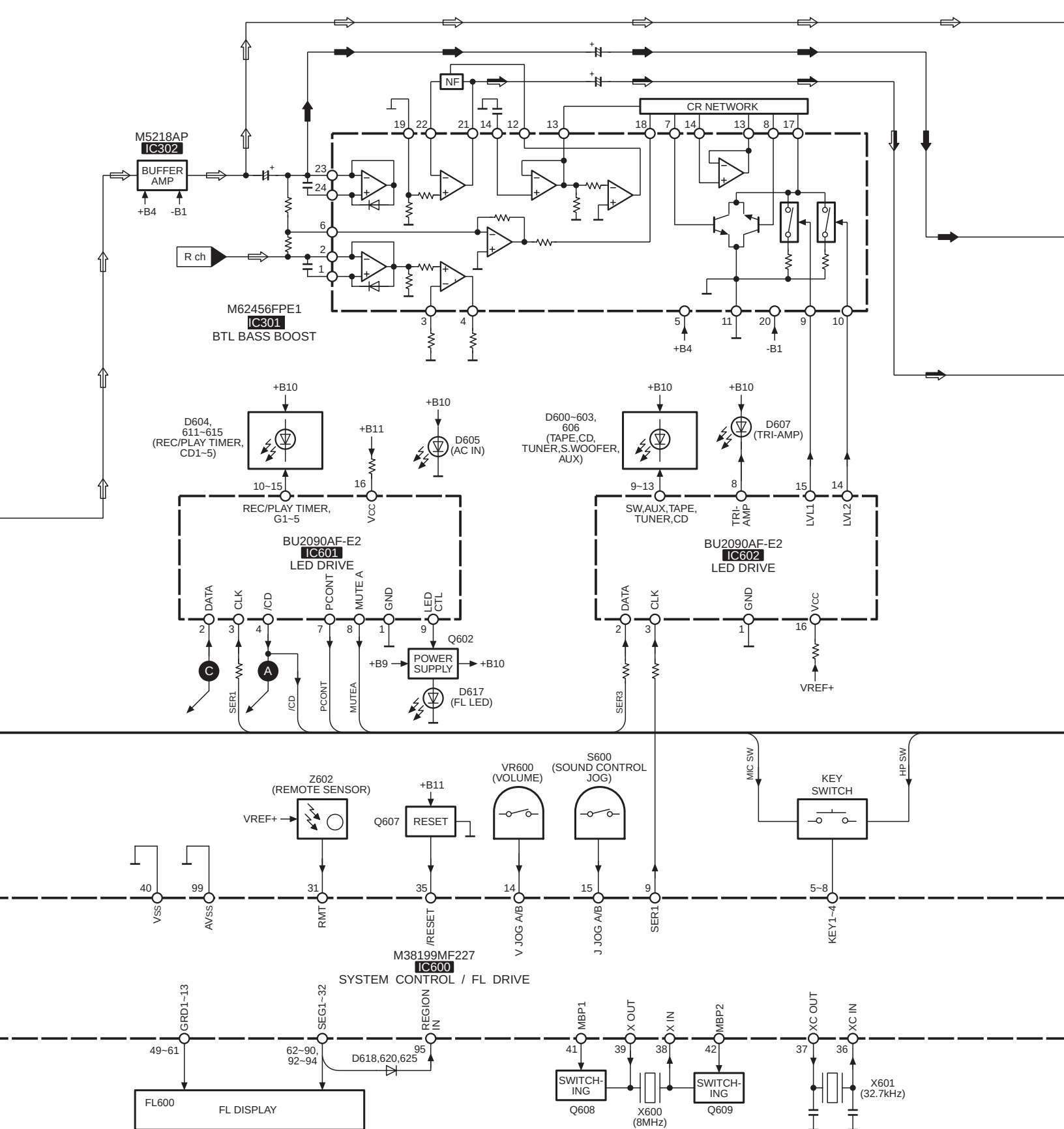
PLL FREQUENCY SYNTHESIZER (Bottom Section):

- Core Components:** LC72131MDTRM IC102, PHASE DETECTOR CHARGE PUMP, REFERENCE DIVIDER, SWALLOW COUNTER (1/6, 1/17, 4 BIT), 12 BIT PROGRAMMABLE DIVIDER, DATA SHIFT REGISTER LATCH, UNIVERSAL COUNTER, and POWER ON RESET.
- Inputs:** FM IN, AM IN, and a 7.2MHz crystal (X103).
- Outputs:** PLLCE, PLLDA, PLLCK, and DO/ST.
- Power:** The circuit is powered by +B4 (VDD) and +B3 (VSS).

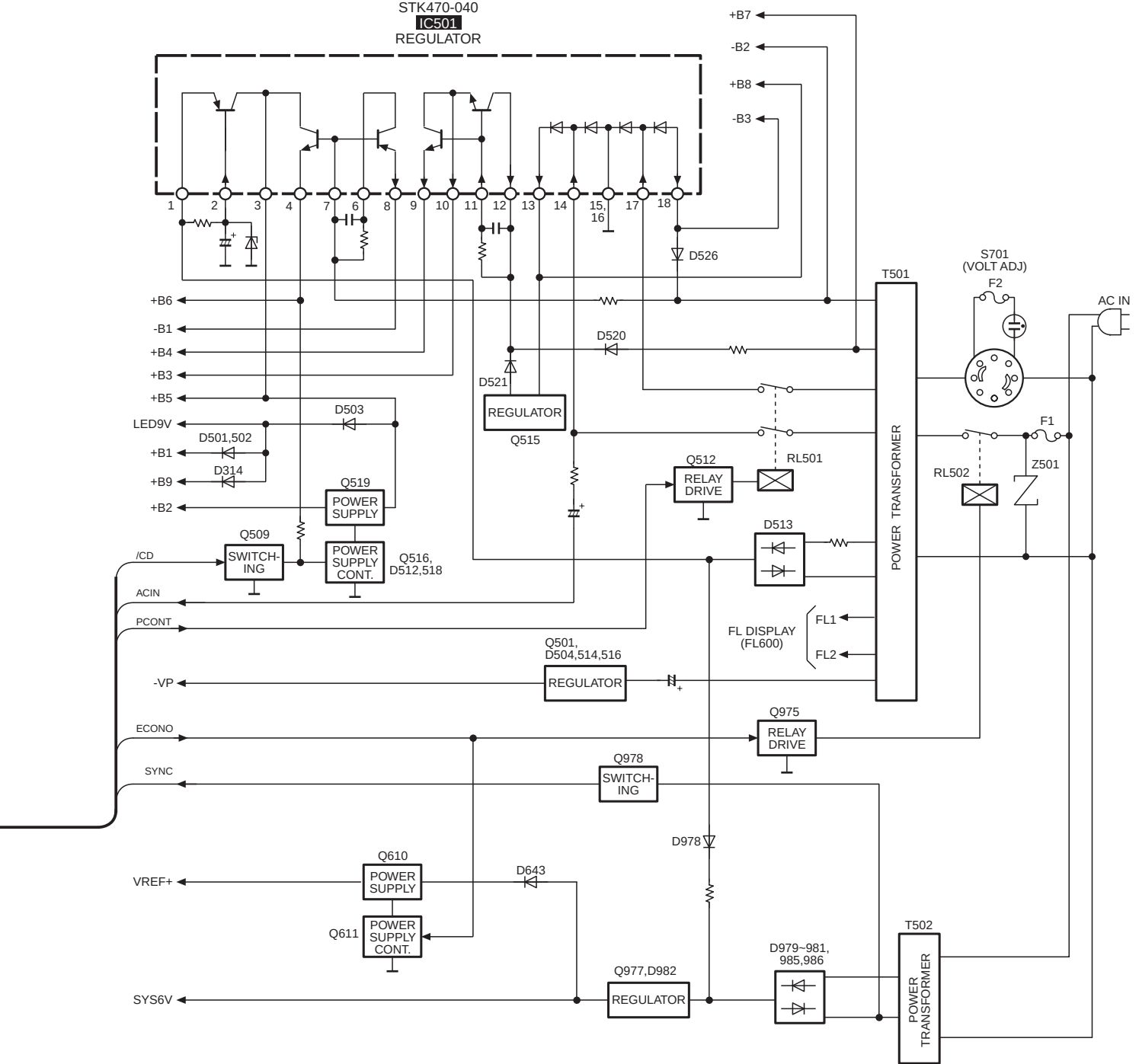
The circuit is designed to receive both FM and AM signals, with a PLL for frequency synthesis and a data shift register for control logic. The output is connected to an Rch (Receiver Channel) output.







STK470-040
IC501
REGULATOR



Notes

- 1) : CD PLAYBACK SIGNAL LINE
 : FM SIGNAL LINE
 : FM OSC SIGNAL LINE
 : AM SIGNAL LINE
 : AM OSC SIGNAL LINE
 : TAPE PLAYBACK SIGNAL LINE
 : TAPE RECORDING SIGNAL LINE
 : FRONT LOW SP DRIVE SIGNAL LINE
 : FRONT MID/HIGH SP DRIVE SIGNAL LINE
 : MIC SIGNAL LINE
- 2) () indicates pin No. of Right channel.