Interface pin definition

| Pin No. | Symbol | Pin Description |
|---------|--------|-----------------|
| 1 | VG | VG is the LCD driving voltage for segment circuits at positive frame |
| 2 | XV0 | XV0 is the LCD driving voltage for common circuits at positive frame |
| 3 | V0 | V0 is the LCD driving voltage for common circuits at negative frame |
| 4 | VSS | Ground |
| 5 | VDD | Power supply（+3.0） |
| 6 | SDA | Serial data input |
| 7 | SCL | Serial clock input |
| 8 | A0 | Data or command select signal input |
| 9 | RES | A reset pin. |
| 10 | CS | Chip select signal input(low active) |

Mechanical Parameters

| Item | standard value | unit |
|------|----------------|------|
| Module Dimension | 59.00X44.30 | mm |
| Viewing Area | 54.20X32.50 | mm |
| Active Area | 47.96X26.53 | mm |
| Dot Pitch | 0.035 | mm |
| Dot Size | 0.38X0.34 | mm |

Electrical Characteristics

| Symbol | Parameter | Conditions | Min. | Type. | Max. | Unit |
|--------|-----------|------------|------|-------|------|------|
| VDD1 | Operating voltage(1) | | 1.7 | - | 3.3 | V |
| VDD2 | Operating voltage(2) | | 2.4 | - | 3.3 | V |
| VDD3 | Operating voltage(3) | | 2.4 | - | 3.3 | V |
| VIHC | Input High-level voltage | | 0.7*VDD1 | - | VDD1 | V |
| VILC | Input Low-level voltage | | VSS1 | - | 0.3*VDD1 | V |
| VOHC | Input High-level voltage | IOUT=1mA,VDD1=1.8V | 0.8*VDD1 | - | VDD1 | V |
| VOLC | Input Low-level voltage | IOUT=-1mA,VDD1=1.8V | VSS1 | - | 0.2*VDD1 | V |

Absolute Maximum Ratings

| Symbol | Parameter | Min. | Max | Unit |
|--------|-----------|------|-----|------|
| VDD1 | Digital Power Supply Voltage | -0.3 | +3.6 | V |
| VDD2,VDD3 | Analog Power Supply Voltage | -0.3 | +3.6 | V |
| V0-XV0 | LCD Power Supply Voltage | -0.3 | +16 | V |
| VG | LCD Power Supply Voltage | -0.3 | +3.6 | V |
| VM | LCD Power Supply Voltage | -0.3 | VDD2 | V |
| Vi | Input Voltage | -0.3 | VDD1+0.3 | V |
| TOPR | Operatingtemperature | -30 | +85 | ℃ |
| TSTR | Storage temperature | -55 | +125 | ℃ |