

# RTAN430X SERIES

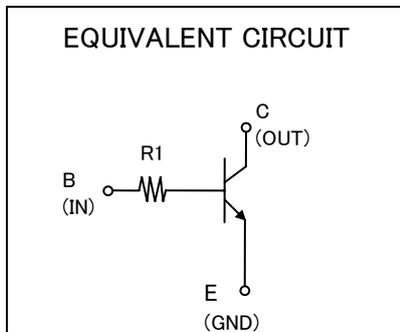
TRANSISTOR WITH RESISTOR  
FOR MUTING APPLICATION  
SILICON NPN EPITAXIAL TYPE

## FEATURE

- Built-in bias resistor ( $R1=4.7k\Omega$ )
- Small package for easy mounting.
- High reverse hFE
- Small collector to emitter saturation voltage.  
 $V_{CE(sat)}=10mV(TYP.)(@I_C=10mA/I_B=0.5mA)$
- Low on Resistance  
 $R_{on}=0.80\Omega(TYP.)(@V_I=5V)$

## APPLICATION

muting circuit , switching circuit



## OUTLINE DRAWING

Unit : mm

RTAN430T2 (PRELIMINARY)	RTAN430M
<p>JEITA, JEDEC : — ISAHAYA : T-USM</p> <p>TERMINAL CONNECTOR ① : BASE ② : EMITTER ③ : COLLECTOR</p>	<p>JEITA : SC-70 JEDEC : —</p> <p>TERMINAL CONNECTOR ① : BASE ② : EMITTER ③ : COLLECTOR</p>
<p>JEITA : SC-75A JEDEC : —</p> <p>TERMINAL CONNECTOR ① : BASE ② : EMITTER ③ : COLLECTOR</p>	<p>JEITA : SC-59 JEDEC : Similar to TO-236</p> <p>TERMINAL CONNECTOR ① : BASE ② : EMITTER ③ : COLLECTOR</p>

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## MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING				UNIT
		RTAN430T2	RTAN430U	RTAN430M	RTAN430C	
V <sub>CBO</sub>	Collector to Base voltage	40				V
V <sub>EBO</sub>	Emitter to Base voltage	40				V
V <sub>CEO</sub>	Collector to Emitter voltage	20				V
I <sub>C</sub>	Collector current	400				mA
P <sub>C</sub>	Collector dissipation (Ta=25°C)	125(※)	150	200		mW
T <sub>J</sub>	Junction temperature	+125	+150			°C
T <sub>stg</sub>	Storage temperature	-55~+125		-55~+150		°C

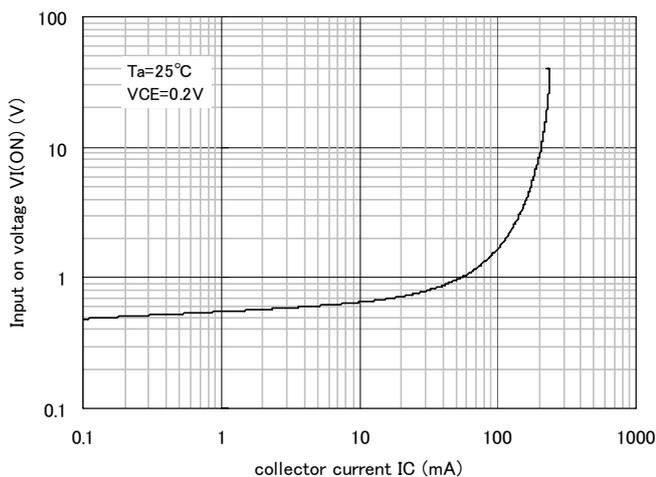
## ELECTRICAL CHARACTERISTICS (Ta=25°C)

※package mounted on 9mm × 19mm × 1mm glass-epoxy substrate.

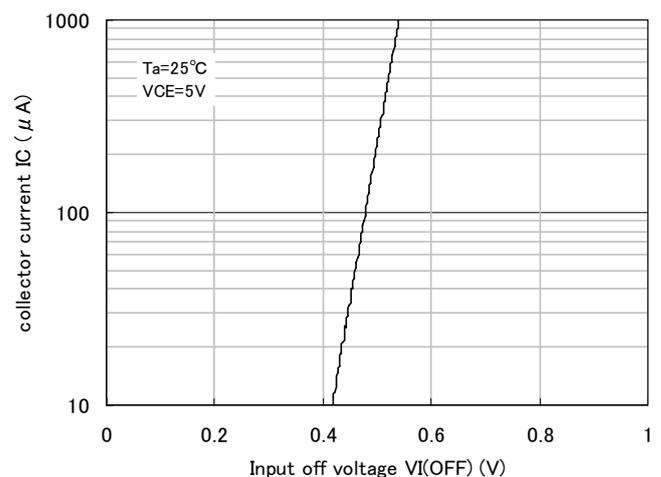
SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
			MIN	TYP	MAX	
V <sub>(BR)CBO</sub>	C to B break down voltage	I <sub>C</sub> =50 μA, I <sub>E</sub> =0mA	40			V
V <sub>(BR)EBO</sub>	E to B break down voltage	I <sub>E</sub> =50 μA, I <sub>C</sub> =0mA	40			V
V <sub>(BR)CEO</sub>	C to E break down voltage	I <sub>C</sub> =1mA, R <sub>BE</sub> =∞	20			V
I <sub>CBO</sub>	Collector cut off current	V <sub>CB</sub> =40V, I <sub>E</sub> =0mA			0.5	μA
I <sub>EBO</sub>	Emitter cut off current	V <sub>EB</sub> =40V, I <sub>C</sub> =0mA			0.5	μA
h <sub>FE</sub>	DC forward current gain	V <sub>CE</sub> =5V, I <sub>C</sub> =10mA	820		2500	—
V <sub>CE(sat)</sub>	C to E saturation voltage	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA		10		mV
R <sub>1</sub>	Input resistance		3.29	4.7	6.11	kΩ
f <sub>T</sub>	Gain band width product	V <sub>CE</sub> =10V, I <sub>E</sub> =-10mA, f=100MHz		38		MHz
R <sub>ON</sub>	Output "ON" resistance	V <sub>I</sub> =5V, R <sub>L</sub> =1kΩ		0.80		Ω

## TYPICAL CHARACTERISTICS

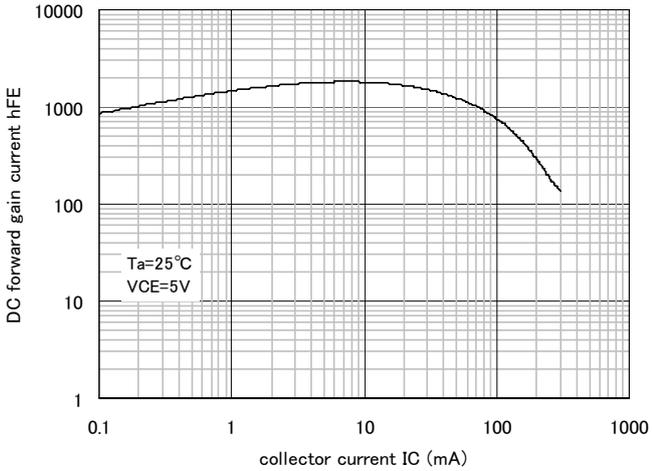
Input on voltage - collector current



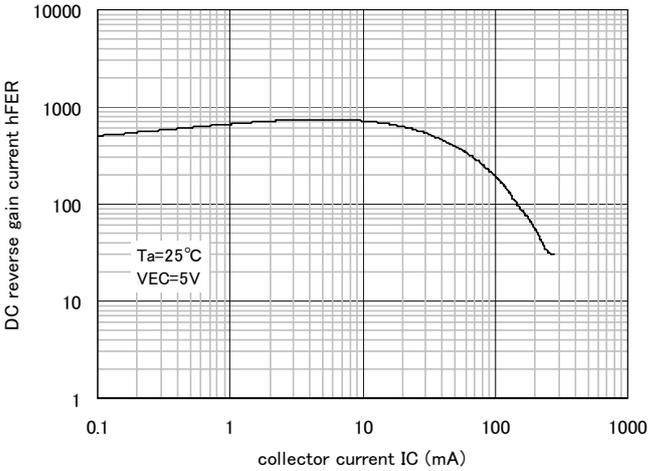
collector current - Input on voltage



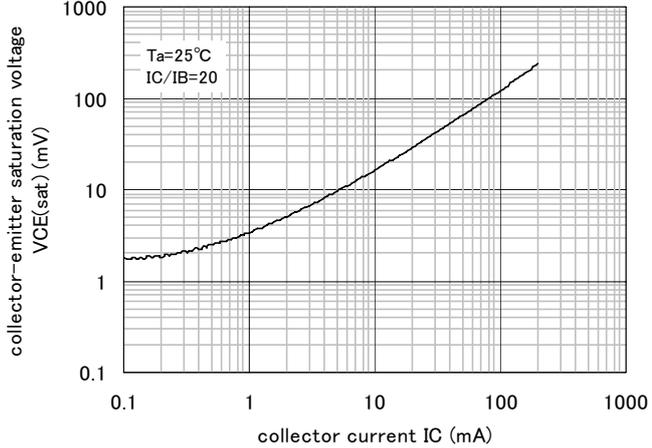
DC forward gain current - collector current



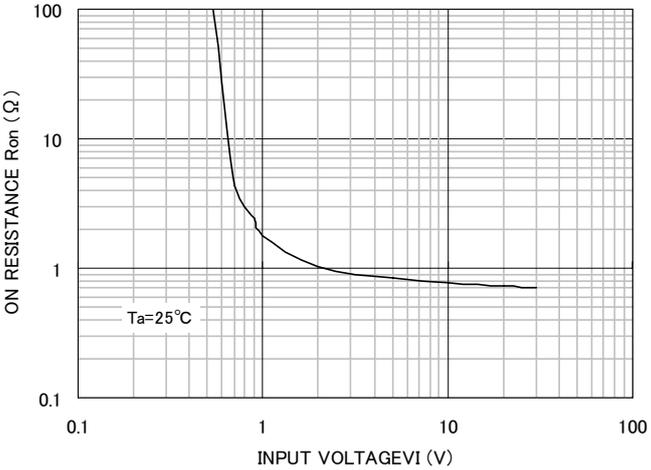
DC reverse gain current - collector current



collector-emitter saturation voltage - collector current



Ron-VIN





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