

The TC9148P is C-MOS LSI developed for use on the infrared remote control transmitter. This LSI has 18 functions, and total 75 commands can be transmitted : 63 commands by the continuous keys of multiple keying is possible and 12 commands by the single shot keys.

- . Wide Range of Operating Supply Voltage Allows Low Voltage Operation ( $V_{DD}$ =2.2~5.0V)
- C-MOS Structure Assures Extremely Low Power Dissipation.
- . Multiple Keying is Possible (Max. Sextet).
- . Less External Parts
- Adaptable to other Models as Code Bits are Available
- An Oscillator can be Constructed only by Connecting an LC or Ceramic Resonater as the Oscillation
- Circuit is Housed.



PIN CONNECTION

MAXIMUM RATINGS  $(Ta=25^{\circ}C)$ 

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>DD</sub> -	6.0	V
Input/Output Voltage	VIN	$V_{SS} = 0.3 \sim V_{DD} = 0.3$	V
Power Dissipation	PD	200	mW
Operating Temperature	Topr	-20~75	°c
Storage Temperature	Tstg	-55~125	°c
TXOUT Output Current	IOUT	-5	mA

	GND		16	
	GUD	7	10	v <sub>DD</sub>
	хт	2	15	TX OUT
	XТ	3	14	TEST
	Кl	4	13	CODE
	К2	5	12	Тз
	Кз 🛛	6	11	T2
•	К4	7	10	Tl
	K5	8	9	К <sub>б</sub>
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DESCRIPTION OF TERMINALS

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PIN No.	SYMBOL	TERMINAL	FUNCTION / OPERATION
1, 16	GND, V <sub>DD</sub>	GND/Power	Supply Voltage Terminal
2, 3	XT, XT	Terminal for OSC	Terminal for OSC, and used for connecting a 455kHz ceramic resonater etc. (with a built-in feedback resistor)
4~9	к <sub>1</sub> ∼к <sub>6</sub>	Key Input Terminal	Key input terminal for Key matrix. 18 keys can be connected at $T_1 \sim T_3 \times K_1 \sim K_6$ (with a built-in pull-down resistor)
10~12	T <sub>1</sub> ~T <sub>3</sub>	Timing Signal Output Terminal	Digit timing output terminal for Key matrix.
13	CODE	Code bit Input Terminal	Terminal for matching code between transmitting and receiving.
14	TEST	Test Terminal	Keep this terminal open.
15	TXOUT	Transmitting Output Terminal	Transmitting signal output. Modulation is made by 12 bits 1 cycle and 38kHz carrier wave.

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TC9148P

ELECTRICAL CHARACTERISTICS (U	Inless ot	herwis	e specified, V <sub>DD</sub> =3.0V	7, Ta=2	5 <sup>0</sup> C)		
CHARACTERISTIC	SYMBOL	TEST CIR-	TEST CONDITION	MIN.	TYP.	MAX.	UNIT

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	CHARA	ACTERISTI	C	SYMBOL	CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Supply Voltage				v <sub>DD</sub>	-	All Function Operation	2.2	-	5.0	V
0per	ating Su	upply Cur	rent	IDD	-	Key ON, Wit <u>hout Load</u>	-	-	1.0	mA
Quiescent Current Comsumption			-	I <sub>DS</sub>		All Key OFF, Stop of OSC	-	-	10	μA
	к <sub>1</sub> ∼к <sub>6</sub>	1~K6 Input Leve		V <sub>IH</sub>	-	-	2.0	-	3.0	v
la 1	CODE	Voltage	"L" Level	VIL	1	-	0	-	0.5	v
Terminal		Input	"H" Level	IIH	-	V <sub>IH</sub> =3.0V	20	30	60	μA
	К <u>1</u> ∼К6	Current	"L" Level	IIL		VIL=OV	-1.0	-	1.0	μA
Input	CODE	Input	"H" Level	IIH	-	V <sub>IH</sub> =3.0V	-1.0	-	1.0	μA
	TEST Current		"L" Level	IIL	-	VIL=0V	20	30	60	μA
nal		Output	"H" Level	I <sub>OH</sub>	-	V <sub>OH</sub> =2.0V	-500	-	. –	μA
Terminal	T1~T3	Current	"L" Level	IOL	-	V <sub>OL</sub> =3.0V	50	-	-	μA
	-	Output	"H" Level	I <sub>OH</sub>	-	V <sub>OH</sub> =2.0V	-0.1	-	-	mA
Outp	TX Output Current		"L" Level	I <sub>OL</sub>	-	V <sub>OL</sub> =2.0V	1.0	-	_	mA
osc	Feedbacl	<pre> Resisto</pre>	r	Rf	-	-		500	-	kΩ
Oscillation Frequency			fosc	-	-	400	455	600	kHz	

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## FUNCTIONAL DESCRIPTION

### I. OSCILLATION CIRCUIT

As the self-bias type amplifier by means of C-MOS inverter has been housed, the oscillation circuit can be constructed when an LC or ceramic resonater is connected.



CERAMIC RESONATOR KBR-455B KYOCERACO. Ltd OR EQUIVALENT C<sub>1</sub>, C<sub>2</sub>=50~150pF



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When oscillation frequency is set at 455kHz, carrier wave of transmitting signal is set at 38kHz, oscillation of the oscillation circuit is kept stopped unless the keys are operated, thus reducing power consumption.

2. KEY INPUT

18 keys can be connected by Key input  $K_{1}\sim$  K6 and  $6\times3$  matrix by means of timing signal  $T_{1}\sim T_{3}.$ 

Multiple keying is possible for the keys connected to T1 line up to sextet, and all key inputs are output. (Output becomes continuous pulses.)

Between the timing signal lines, priority has been decided in order of T1, T2 and T3. The keys connected to T2 and T3 lines have priority and input is made through more than 2 keys, single signal is preferentially output in order of  $K_1 \sim K_6$ .

Further, the keys connected to T<sub>2</sub> and T<sub>3</sub> lines are for single signals and no second signal is transmitted unless input is made again after the key is released once.

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### KEY MATRIX

. Key No.1~6

Continuous key output with it pressed, and multiple keying is possible.

1. A. M. M.

. Key No.7~18

These keys are the single-shot keys and when input is made, signal is output only one time.

### 3. TRANSMISSION COMMAND

Transmission command is in one word 12-bits configuration.

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 $C_1 \sim C_3$  are code bits adaptable to many models, H, S1 and S2 are continuous signal and single-shot signal codes, and  $D_1 \sim D_6$  are Key Input data codes in 6-bits.

	C1	C2	C3	H	s <u>1</u>	S2	D1	D2	D3	D4	D5	D6	
	c	ODE B	ĪT	SIN	TINUO GLE-SI E	HOT	r a 25.		Y INP	UT CO	DE	<b>ت</b> ــــــــــــــــــــــــــــــــــــ	
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TOSHIE	3 <b>A</b> ss		WAR DE LE LE DE LE			in Distante		52 <i>—</i>		0.454090508		**************************************	

4. DATA CODE

KEY	1					OUTPUT				OUTPUT											
No.	H	s <u>1</u>	S2	D1	D2	D3	D4	D5	D6	FORM	No.	Н	s <sub>1</sub>	s2	D1	D2	D3	D4	D5	D6	FORM
1	1	0	0	1	0	0	0	0	0	CONTINUOUS	10	0	1	0	0	0	0	1	0	0	SINGLE-SHOT
2	1	0	0	0	1	0	0	0	0	11	11	0	1	0	0	0	0	0	1	0	11
3	1	0	0	0	0	1	0	0	0	"	12	0	1	0	0	0	0	0	0	1	11
4	1	0	0	0	0	0	1	0	0	11	13	0	0	1	1	0	0	0	0	0	11
5	1	0	0	0	0	0	0	1	0		14	0	0	1	0	1	0	0	0	0	ti
6	1	0	0	0	0	0	0	0	1	: 11	15	0	0	.1	0	0	1	0	0	0	11
7	0	1	0	1	0	0	٥,	0	0	SINGLE-SHOT	16	0	0	1	0	0	0	1	0	0	11
8	0	1	0	0	1	0	0	0	0	н	17	0	0	1	0	0	0	0	1	0	11
9	0	1	0	0	0	1	Ó	0	0	11	18	0	0	1	0	0	0	0	0	1	11

As the multiple keying is possible, Key No.  $1 \sim 6$  are capable of output 63 commands through a combination of  $D_1 \sim D_6$  data.

Key No.  $7 \sim \! 18$  are the single-shot keys for output 12 commands, and 75 commands can be output through a combination of continuous key ( multiple keying is possible ) and Single-Shot key.

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5. CODE BITS (C1, C2, C3)

Code bit can be made at one terminal with diodes connected through T1~T3 timing terminals.



Data of C<sub>1</sub>, C<sub>2</sub> and C<sub>3</sub> code bit become "1" when diodes are connected to CODE Terminal through Timing Signal. Terminals T<sub>1</sub>~T<sub>3</sub>, and "0" when not connected. (In the above diagram, C<sub>1</sub>, C<sub>2</sub> and C<sub>3</sub> are 1, 1 and 1 data.) ' The TC9148P has 3 code bits. However, the TC9149P that is a receiving IC (DIP 16 PIN) and the TC9150P (DIP 24 PIN) are able to use only C<sub>2</sub> and C<sub>3</sub>, and C<sub>1</sub> and C<sub>2</sub> 2 code bits, respectively.

Therefore, diodes must be connected so that code bit data of the TC9148P agreement with the receiving IC.



TOSHIBA

C<sub>1</sub>, C<sub>2</sub> ..... TC9150P C<sub>2</sub>, C<sub>3</sub> ..... TC9149P

\* CODE BIT, "O", "O" CANNOT BE USED.

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Note. For C3 and C1 code bit data not used on the TC9150P and TC9149P, it is necessary to transmit "1" and diodes must be so connected.

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Basic transmitting waveform is 12-bits serial data in configuration as shown above.

The time of each bit "a"is decided as shown below by oscillation frequency  $f_{\rm osc}$  by means of  $X_T$  and  $\overline{X_T}$  .

$$a = (1/f_{osc}) \times 192$$
 (sec)

6.2 DISTINCTION OF BIT "O" AND "1"





