

Everything You Always Wanted To Know About The ICL8038

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Introduction

The 8038 is a function generator capable of producing sine, square, triangular, sawtooth and pulse waveforms (some at the same time). Since its introduction, marketing and application engineers have been manning the phones explaining the care and feeding of the 8038 to customers worldwide. This experience has enabled us to form articulate responses to the most frequently asked questions. So, with data sheet and breadboard in hand, read on and be enlightened.

Question 1

I want to sweep the frequency externally but can only get a range of 100:1 (or 50:1, or 10:1). Your data sheet says 1000:1. How much sweep range can I expect?

Answer

Let's look at what determines the output frequency. Start by examining the circuit schematic at pin 8 in the upper left hand corner. From pin 8 to pin 5 we have the emitter-base of NPN Q_1 and the emitter-base of PNP Q_2 . Since these two diode drops cancel each other (approximately), the potential at pins 8, 5, and 4 are the same. This means that the voltage from V_+ to pin 8 is the same as the voltage across external resistors R_A and R_B . This is a textbook example of a voltage across two resistors which produce two currents to charge and discharge a capacitor between two fixed voltages. This is also a linear system. If the voltage across the resistors is dropped from 10V to 1V, the frequency will drop by 10:1. Changing from 1V to 0.1V will also change the frequency by 10:1. Therefore, by causing the voltage across the external resistors to change from say 10V to 10mV, the frequency can be made to vary at least 1000:1. There are, however, several factors which make this large sweep range less than ideal.

Question 2

You say I can vary the voltage on pin 8 (FM sweep input) to get this large range, yet when I short pin 8 to V_+ (pin 6), the ratio is only around 100:1.

Answer

This is often true. With pin 8 shorted to V_+ , a check on the potentials across the external R_A and R_B will show 100mV or more. This is due to the V_{BE} mismatch between Q_1 and Q_2 (also Q_1 and Q_3) because of the geometries and current levels involved. Therefore, to get smaller voltages across these resistors, pin 8 must be raised above V_+ .

Question 3

How can I raise pin 8 above V_+ without a separate power supply?

Answer

First of all, the voltage difference need only be a few hundred millivolts so there is no danger of damaging the 8038. One way to get this higher potential is to lower the supply voltage on the 8038 and external resistors. The simplest way to do this is to include a diode in series with pin 6 and resistors R_A and R_B . See Figure 1. This technique should increase the sweep range to 1000:1.

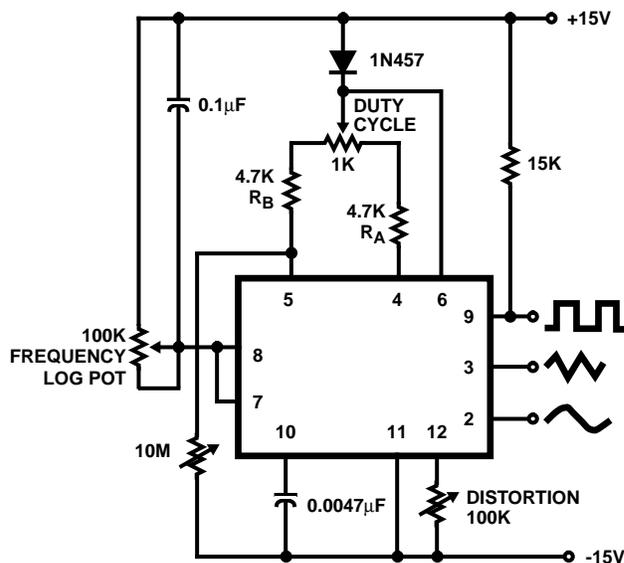


FIGURE 1. VARIABLE AUDIO OSCILLATOR, 20Hz TO 20kHz

Question 4

O.K., now I can get a large frequency range, but I notice that the duty cycle and hence my distortion changes at the lowest frequencies.

Answer

This is caused partly by a slight difference in the V_{BE} s of Q_2 and Q_3 . In trying to manufacture two identical transistors, it is not uncommon to get V_{BE} differences of several millivolts or more. In the standard 8038 connection with pins 7 and 8 connected together, there are several volts across R_A and R_B and this small mismatch is negligible. However, in a swept mode

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with the voltage at pin 8 near V_+ and only tens of millivolts across R_A and R_B , the V_{BE} mismatch causes a larger mismatch in charging currents, hence the duty cycle changes. For lowest distortion then, it is advisable to keep the minimum voltage across R_A and R_B around 100mV. This would of course, limit the frequency sweep range to around 100:1.

Question 5

I have a similar duty cycle problem when I use high values of R_A and R_B . What causes this?

Answer

There is another error term which becomes important at very low charge and discharge currents. This error current is the emitter current of Q_7 . The application note on the 8038 gives a complete circuit description, but it is sufficient to know that the current charging the capacitor is the current in R_A which flows down through diode Q_9 and into the external C. The discharge current is the current in R_B which flows down through diode Q_8 . Adding to the Q_8 current is the current of Q_7 which is only a few microamperes. Normally, this Q_7 current is negligible, but with a small current in R_B , this current will cause a faster discharge than would be expected. This problem will also appear in sweep circuits when the voltage across the external resistors is small.

Question 6

How can I get the lowest distortion over the largest frequency sweep range.

Answer

First of all, use the largest supply voltage available ($\pm 15V$ or $+30V$ is convenient). This will minimize V_{BE} mismatch problems and allow a wide variation of voltage on pin 8. The potential on pin 8 may be swept from V_{CC} (and slightly higher) to $2/3 V_{CC} + 2V$ where V_{CC} is the total voltage across the 8038. Specifically for $\pm 15V$ supplies ($+30V$), the voltage across the external resistors can be varied from 0V to nearly 8V before clipping of the triangle waveform occurs.

Second, keep the maximum currents relatively large (1mA or 2mA) to minimize the error due to Q_7 . Higher currents could be used, but the small geometry transistors used in the 8038 could give problems due to $V_{CE(SAT)}$ and bulk resistance, etc.

Third, and this is important, use two separate resistors for R_A and R_B rather than one resistor with pins 4 and 5 connected together. This is because transistors Q_2 and Q_3 form a differential amplifier whose gain is determined by the impedance between pins 4 and 5 as well as the quiescent current. There are a number of implications in the differential amplifier connection (pins 4 and 5 shorted). The most obvious is that the gain determines the way the currents split between Q_2 and Q_3 . Therefore, any small offset or differential voltage will cause a marked imbalance in the charge and discharge currents and hence the duty cycle. A more subtle result of this connection is the effective capacitance at pin 10. With pins 4 and 5 connected together, the "Miller Effect" as well as the compound transistor connection of Q_3 and Q_5 can produce several hundred picofarads at pin 10, seriously limiting the highest frequency of oscillation. The effective capacitance would have to be considered important in determining what value of external C would result in a particular frequency of

oscillation. The single resistor connection is fine for very simple circuits, but where performance is critical, the two separate resistors for R_A and R_B are recommended.

Finally, trimming the various pins for lowest distortion deserves some attention. With pins 7 and 8 connected together and the pot at pin 7 and 8 externally set at its maximum, adjust the ratio of R_A and R_B for 50% duty cycle. Then adjust a pot on pin 12 or both pins 1 and 12 depending on minimum distortion desired. After these trims have been made, set the voltage on pin 8 for the lowest frequency of interest. The principle error here is due to the excess current of Q_7 causing a shift in the duty cycle. This can be partially compensated for by bleeding a small current away from pin 5. The simplest way to do this is to connect a high value of resistance ($10M\Omega$ to $20M\Omega$) from pin 5 to V_- to bring the duty cycle back to 50%. This should result in a reasonable compromise between low distortion and large sweep range.

Question 7

This waveform generator is a piece of junk. The triangle wave is non-linear and has large glitches when it changes slope.

Answer

You're probably having trouble keeping the constant voltage across R_A and R_B really constant. The pulse output on pin 9 puts a moderate load on both supplies as it switches current on and off. Changes in the supply reflect as variations in charging current, hence non-linearity. Decoupling both power supply pins to ground right at the device pins is a good idea. Also, pins 7 and 8 are susceptible to picking up switching transients (this is especially true on printed circuit boards where pins 8 and 9 run side by side). Therefore, a capacitor (0.1 μ F or more) from V_+ to pin 8 is often advisable. In the case when the pulse output is not required, leave pin 9 open to be sure of minimizing transients.

Question 8

What is the best supply voltage to use for lowest frequency drift with temperature?

Answer

The 8038AM, 8038AC, 8038BM and 8038BC are all temperature drift tested at $V_{CC} = +20V$ (or $\pm 10V$). A curve in the lower right hand corner of Page 4 of the data sheet indicates frequency versus temperature at other supply voltages. It is important to connect pins 7 and 8 together.

Question 9

Why does connecting pin 7 to pin 8 give the best temperature performance?

Answer

There is a small temperature drift of the comparator thresholds in the 8038. To compensate for this, the voltage divider at pin 7 uses thin film resistors plus diffused resistors. The different temperature coefficients of these resistors causes the voltage at pins 7 and 8 to vary $0.5mV/^\circ C$ to maintain overall low frequency drift at $V_{CC} = 20V$. At higher supply voltages, e.g., $\pm 15V$ ($+30V$), the threshold drifts are smaller compared with the total supply voltage. In this case, an externally applied constant voltage at pin 8 will give reasonably low frequency drift with temperature.

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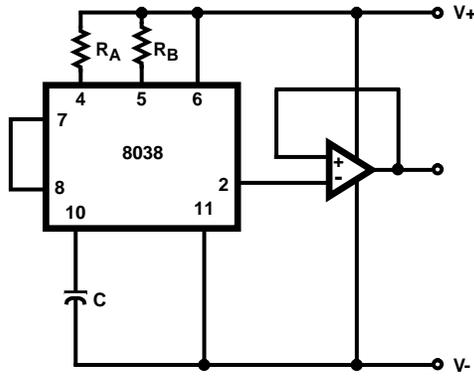


FIGURE 3A.

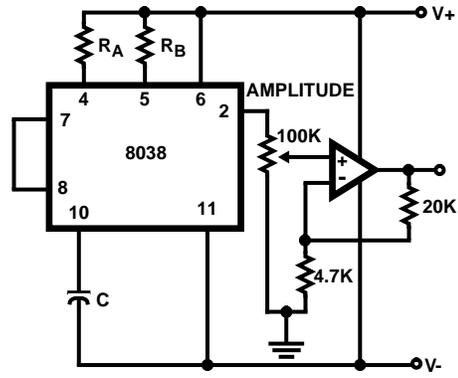


FIGURE 3B.

FIGURE 3. SINEWAVE OUTPUT BUFFER AMPLIFIERS

Schematic Diagram

