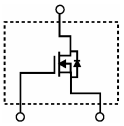
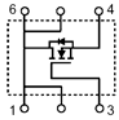
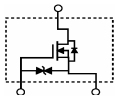
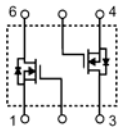
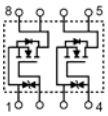
SURFACE MOUNT DEVICES FOR HYBRID APPLICATIONS

PLASTIC MATERIAL USED CARRIES UL 94V-0

TYPE	Marking	Drain to Source Voltage	Gate to Source Voltage	Drain Current	Static Drain to Source On Resistance		Max. Gate Threshold Voltage	Zero Gate Voltage Drain Current		Outline No.	Equivalent Circuit Diagram
		V_{DSS}	V_{GSS}	$I_{D(DC)}$	$R_{DS(ON)}$ @ I_D / V_{GS}		$V_{GS(th)}$	I_{DSS} @ V_{DS},V_{GS}=0V			
		V	V	mA	ohmS	mA / V	V	uA	V		
N CHANNEL ENHANCEMENT MOS FET											
2N7002M1PT	72	60	+/-20	115	7.5	500/10	2.5	1	60	FBPT-723	
CHT2302WPT	22	20	+/-8	2800	0.085	3600/4.5	0.7(Min)	1	16	SC-70	
2N7002EPT	702E	60	+/-20	250	4	100/4	2.5	1	60	SC-70	
2N7002TPT	702	60	+/-20	250	4	100/4	2.5	1	60	SC-75	
CHT2302PT	02	20	+/-8	2800	0.085	3600/4.5	0.7(Min)	1	16	SOT-23/SC-59	
CHT2324PT	24	20	+/-12	4200	0.045	4200/4.5	1	1	16	SOT-23/SC-59	
CHT2312PT	12	20	+/-8	4500	0.033	5000/4.5	1	1	20	SOT-23/SC-59	
CHM2310PT	10	30	+/-12	4800	0.040	4000/4.5	1.4	1	30	SC-59	
CHM2304PT	04	30	+/-20	2800	0.120	2000/4.5	3	0.5	30	SC-59	
CHM2316PT	16	30	+/-20	4800	0.050	4900/4.5	3	1	30	SC-59	
CHM2314PT	14	30	+/-20	4000	0.070	3000/4.5	3	1	24	SC-59	
CHM2342PT	42	40	+/-20	4200	0.058	3300/4.5	3	1	40	SC-59	
CHM2362PT	62	60	+/-20	3000	0.100	2400/4.5	3	1	60	SC-59	
CHT870PT	BT	60	+/-20	250	5	250/10	3	1	60	SOT-23	
CHT170PT	AT	60	+/-20	500	5	200/10	3	1	60	SOT-23	
CHM310PT	310	100	+/-20	170	9	170/4.5	2.8	0.1	100	SOT-23	
CHT100PT	AT	30	+/-20	1100	0.170	500/4.5	3	1	24	SOT-23/SC-59	
2N7002PT	7002	60	+/-20	250	4	100/4	2.5	1	60	SOT-23/SC-59	
N CHANNEL ENHANCEMENT MOS FET											
CHM7400SPT	740S	30	+/-12	2800	0.085	2300/4.5	1.6	1	24	SC-88	
CHM2316QPT	2316	30	+/-20	6000	0.050	4900/4.5	3	1	30	SC-74	
N CHANNEL ENHANCEMENT MOS FET											
2N7002ESEPT	PK1	60	+/-20	500	4.5	500/10	2.5	1	48	SC-70	
CHM2308ESPT	2308	20	+/-12	5400	0.036	4300/2.5	1.2	1	20	SC-59	
CHM1592PT	1592	60	+/-20	500	2	300/10	2.0	10	60	SOT-23	
CHM1273PT	1273	60	+/-20	2000	0.065	500/10	2.5	10	60	SOT-23	
2N7002ESPT	PK1	60	+/-20	300	5.3	75/4.5	2.5	1	48	SOT-23/SC-59	
DUAL N CHANNEL ENHANCEMENT MOS FET											
2N7002VPT	V7	60	+/-20	280	7.5	50/5	2.5	1	60	SOT-563	
2N7002SPT	702S	60	+/-20	250	4	100/4	2.5	1	60	SC-88	
2SK3541SPT	97	30	+/-20	100	8	10/4	1.65	1	30	SC-88	
CHM8207JPT	8207	20	+/-12	6000	0.020	6000/4.5	1.5	1	20	SO-8	
CHM8208JPT	8208	20	+/-12	7000	0.022	7000/4.5	1.2	1	20	SO-8	

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SURFACE MOUNT DEVICES FOR HYBRID APPLICATIONS

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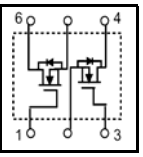
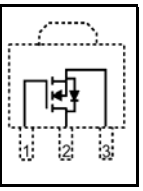
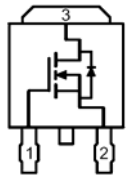
TYPE	Marking	Drain to Source Voltage	Gate to Source Voltage	Drain Current	Static Drain to Source On Resistance		Max. Gate Threshold Voltage	Zero Gate Voltage Drain Current		Outline No.	Equivalent Circuit Diagram
		V_{DSS}	V_{GSS}	$I_{D(DC)}$	$R_{DS(ON)}$ @ I_D / V_{GS}		$V_{GS(th)}$	I_{DSS} @ V_{DS},V_{GS}=0V			
		V	V	mA	ohmS	mA / V	V	uA	V		
DUAL N CHANNEL ENHANCEMENT MOS FET											
2N7002DSPT	72DS	50	+/-20	510	4	350/4.5	2.5	1	40	SC-74	
2N7002SSPT	72SS	50	+/-20	510	4	350/4.5	2.5	1	40	SC-88	
N CHANNEL ENHANCEMENT MOS FET											
CHM3055LXPT	3055	60	+/-20	3700	0.120	3700/4.5	2	1	60	SOT-89	
CHM04N10ZPT	4N10	100	+/-20	3000	0.280	1300/6	4	1	100	SOT-223	
CHM51A3ZPT	51A3	30	+/-20	9500	0.028	17500/4.5	3	1	30	SOT-223	
CHM453NZPT	453N	30	+/-20	8000	0.042	6700/4.5	3	1	24	SOT-223	
CHM451ANZPT	451AN	30	+/-20	7200	0.050	6000/4.5	3	1	24	SOT-223	
CHM456NZPT	456N	60	+/-20	5000	0.075	3800/5	3	1	55	SOT-223	
CHM3055ZPT	3055	60	+/-20	4000	0.100	4000/10	4	10	48	SOT-223	
N CHANNEL ENHANCEMENT MOS FET											
CHM21A2PAPT	-	20	+/-12	20000	0.04	8000/4.5	1.5	1	20	TO-252A	
CHM9926PAPT	-	20	+/-12	26000	0.03	8000/4.5	1.5	1	20	TO-252A	
CHM41A2PAPT	-	20	+/-12	36000	0.02	10700/4.5	1.5	1	20	TO-252A	
CHM62A2PAPT	-	20	+/-12	48000	0.012	18000/4.5	1.2	1	20	TO-252A	
CHM65A3PAPT	-	25	+/-20	38000	0.026	6000/4.5	3	1	25	TO-252A	
CHM75A3PAPT	-	25	+/-20	60000	0.02	24000/4.5	3	1	25	TO-252A	
CHM55A3PAPT	-	25	+/-20	60000	0.013	30000/4.5	3	1	25	TO-252A	
CHM78A3PAPT	-	25	+/-20	70000	0.011	30000/4.5	3	1	25	TO-252A	
CHM85A3PAPT	-	25	+/-20	80000	0.009	30000/4.5	3	1	25	TO-252A	
CHM3700PAPT	-	30	+/-20	12000	0.13	6000/4.5	2.5	1	30	TO-252A	
CHM3055LAPAPT	-	30	+/-20	12000	0.09	12000/5	2.5	1	30	TO-252A	
CHM603ALPAPT	-	30	+/-20	20000	0.04	10000/4.5	3	1	24	TO-252A	
CHM21A3PAPT	-	30	+/-20	20000	0.07	12000/4.5	2.5	1	30	TO-252A	
CHM3252PAPT	-	30	+/-20	25000	0.039	3500/4.5	3	1	30	TO-252A	
CHM51A3PAPT	-	30	+/-20	35000	0.028	17500/4.5	3	1	30	TO-252A	
CHM3120PAPT	-	30	+/-20	36000	0.022	29000/4.5	3	1	30	TO-252A	
CHM3172PAPT	-	30	+/-20	36000	0.028	8000/4.5	3	1	30	TO-252A	
CHM61A3PAPT	-	30	+/-20	40000	0.02	18000/4.5	3	1	30	TO-252A	
CHM6030LPAPT	-	30	+/-20	40000	0.022	18000/4.5	3	1	24	TO-252A	
CHM703ALPAPT	-	30	+/-20	40000	0.032	10000/4.5	3	1	24	TO-252A	
CHM63A3PAPT	-	30	+/-20	55000	0.014	24000/4.5	3	1	30	TO-252A	
CHM6031LPAPT	-	30	+/-20	55000	0.015	5000/4.5	3	1	24	TO-252A	
CHM62A3PAPT	-	30	+/-20	55000	0.016	11000/4.5	3	1	30	TO-252A	
CHM3070PAPT	-	30	+/-20	62000	0.0135	24000/4.5	3	1	30	TO-252A	
CHM73A3PAPT	-	30	+/-20	65000	0.013	30000/4.5	3	1	25	TO-252A	
CHM71A3PAPT	-	30	+/-20	65000	0.014	13000/5	3	1	30	TO-252A	
CHM72A3PAPT	-	30	+/-20	70000	0.011	15000/4.5	3	1	30	TO-252A	
CHM83A3PAPT	-	30	+/-20	80000	0.009	30000/4.5	3	1	30	TO-252A	
CHM4204PAPT	-	40	+/-20	24000	0.045	5000/4.5	4	1	32	TO-252A	
CHM4060APAPT	-	60	+/-20	15000	0.085	7500/10	4	25	60	TO-252A	
CHM6426PAPT	-	60	+/-20	16000	0.085	6400/4.5	3	1	60	TO-252A	
CHM20N06PAPT	-	60	+/-20	20000	0.075	15000/4.5	3	1	55	TO-252A	
CHM6060RPAPT	-	60	+/-20	30000	0.025	24000/10	4	25	60	TO-252A	
CHM50N06PAPT	-	60	+/-20	36000	0.023	15000/10	4	1	60	TO-252A	
CHM13N07PAPT	-	70	+/-20	11000	0.153	5500/5	2.5	1	60	TO-252A	
CHM12N10PAPT	-	100	+/-20	11000	0.18	6000/10	4	1	100	TO-252A	

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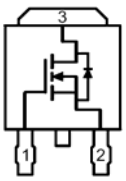
		V_{DSS}	V_{GSS}	$I_{D(DC)}$	$R_{DS(ON)}$ @ I_D / V_{GS}		$V_{GS(th)}$	I_{DSS} @ V_{DS},V_{GS}=0V			
		V	V	mA	ohmS	mA / V	V	uA	V		
N CHANNEL ENHANCEMENT MOS FET											
CHM1710PAPT	-	100	+/-20	17000	0.085	15000/10	4	1	100	TO-252A	
CHM540APAPT	-	100	+/-20	25000	0.049	18000/10	4	25	100	TO-252A	
CHM1012LPAPT	-	120	+/-20	10000	0.12	10000/10	3	25	120	TO-252A	
CHM1012PAPT	-	120	+/-20	10000	0.12	10000/10	4	25	120	TO-252A	
CHM630PAPT	-	200	+/-20	7800	0.36	5000/10	4	25	160	TO-252A	
CHM634PAPT	-	250	+/-30	6700	0.45	5100/10	4	25	250	TO-252A	
CHM02N6GPAPT	-	600	+/-30	1900	5.5	1000/10	4	25	600	TO-252A	
CHM01N6PAPT	-	650	+/-30	900	15	400/10	4	1	600	TO-252A	
CHM02N7PAPT	-	700	+/-30	1600	6.6	1000/10	4	25	700	TO-252A	
CHM9926NPT	-	20	+/-8	20000	0.03	6000/4.5	1	1	20	D2PAK	
CHM21A2NPT	-	20	+/-12	25000	0.04	8000/4.5	1.5	1	20	D2PAK	
CHM41A2NPT	-	20	+/-12	40000	0.02	20000/4.5	1.5	1	20	D2PAK	
CHM62A2NPT	-	20	+/-12	55000	0.01	25000/4.5	1.2	1	20	D2PAK	
CHM65A3NPT	-	25	+/-20	45000	0.018	24000/4.5	2.5	1	25	D2PAK	
CHM75A3NPT	-	25	+/-20	69000	0.013	29000/4.5	3	1	25	D2PAK	
CHM85A3NPT	-	25	+/-20	90000	0.009	24000/4.5	3	1	25	D2PAK	
CHM6031LNPT	-	30	+/-16	60000	0.015	21000/4.5	3	1	24	D2PAK	
CHM21A3NPT	-	30	+/-20	20000	0.07	12000/4.5	2.5	1	30	D2PAK	
CHM603ALNPT	-	30	+/-20	25000	0.04	10000/4.5	3	1	24	D2PAK	
CHM3120NPT	-	30	+/-20	40000	0.022	32000/4.5	3	1	30	D2PAK	
CHM703ALNPT	-	30	+/-20	40000	0.03	10000/4.5	3	1	24	D2PAK	
CHM51A3NPT	-	30	+/-20	48000	0.028	20000/4.5	3	1	30	D2PAK	
CHM6030LNPT	-	30	+/-20	52000	0.02	21000/4.5	3	1	24	D2PAK	
CHM62A3NPT	-	30	+/-20	60000	0.015	21000/4.5	3	1	30	D2PAK	
CHM61A3NPT	-	30	+/-20	60000	0.019	21000/4.5	3	1	30	D2PAK	
CHM7030LNPT	-	30	+/-20	65000	0.012	28000/4.5	3	1	30	D2PAK	
CHM73A3NPT	-	30	+/-20	65000	0.013	40000/4.5	3	1	25	D2PAK	
CHM63A3NPT	-	30	+/-20	66000	0.014	24000/4.5	3	1	30	D2PAK	
CHM71A3NPT	-	30	+/-20	70000	0.0105	40000/5	3	1	30	D2PAK	
CHM72A3NPT	-	30	+/-20	75000	0.009	37000/4.5	3	1	30	D2PAK	
CHM8030LANPT	-	30	+/-20	75000	0.009	30000/4.5	3	1	30	D2PAK	
CHM76139NPT	-	30	+/-20	75000	0.01	37000/4.5	3	1	30	D2PAK	
CHM83A3NPT	-	30	+/-20	100000	0.008	40000/4.5	3	1	30	D2PAK	
CHM4060ALNPT	-	60	+/-16	15000	0.095	7500/5	2	25	60	D2PAK	
CHM4060ANPT	-	60	+/-20	15000	0.085	7500/10	4	25	60	D2PAK	
CHM20N06NPT	-	60	+/-20	25000	0.075	15000/4.5	3	1	55	D2PAK	
CHM50N06NPT	-	60	+/-20	50000	0.022	50000/10	4	1	54	D2PAK	
CHM6060RNPT	-	60	+/-20	60000	0.025	24000/10	4	25	60	D2PAK	
CHM70N06NPT	-	60	+/-20	70000	0.013	50000/10	4	25	58	D2PAK	
CHM7060RNPT	-	60	+/-20	75000	0.013	40000/10	4	25	60	D2PAK	
CHM80N75NPT	-	75	+/-20	80000	0.013	40000/4.5	4	1	60	D2PAK	
CHM13N10NPT	-	100	+/-20	12800	0.18	6000/10	4	1	100	D2PAK	
CHM540ANPT	-	100	+/-20	36000	0.048	18000/10	4	25	100	D2PAK	
CHM60N10NPT	-	100	+/-20	57000	0.024	30000/10	4	1	80	D2PAK	
CHM1012LNPT	-	120	+/-20	10000	0.12	10000/5	3	25	120	D2PAK	
CHM1012NPT	-	120	+/-20	15000	0.12	10000/10	4	25	120	D2PAK	
CHM630NPT	-	200	+/-20	10000	0.35	5000/10	4	25	160	D2PAK	
CHM640NPT	-	200	+/-20	18000	0.18	9000/10	4	25	160	D2PAK	
CHM634NPT	-	250	+/-30	8100	0.45	5100/10	4	25	250	D2PAK	
CHM740ANPT	-	400	+/-30	10000	0.65	6000/10	4	50	400	D2PAK	



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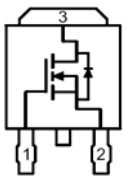
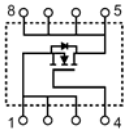
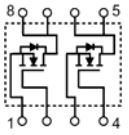
		V	V	mA	ohmS	mA / V	V	uA	V		
N CHANNEL ENHANCEMENT MOS FET											
CHM10N4NPT	-	450	+/-30	10000	0.700	6000/10	4	100	450	D2PAK	
CHM06N5NPT	-	500	+/-30	6600	1	4000/10	4	25	500	D2PAK	
CHM02N6NPT	-	600	+/-30	2000	5	1000/10	4	25	600	D2PAK	
CHM04N6NPT	-	600	+/-30	4000	2.5	2000/10	4	25	600	D2PAK	
CHM09N6NPT	-	600	+/-30	9000	1.2	6000/10	4	50	600	D2PAK	
CHM1165NPT	-	600	+/-30	10000	0.900	5000/10	4	10	600	D2PAK	
CHM02N6ANPT	-	650	+/-30	1500	7.5	800/10	4	25	600	D2PAK	
CHM02N7NPT	-	700	+/-30	1900	6.6	1000/10	4	25	700	D2PAK	
CHM09N7NPT	-	700	+/-30	8000	1.2	5000/10	4	50	700	D2PAK	
N CHANNEL ENHANCEMENT MOS FET											
CHM2082JPT	2082	20	+/-12	11000	0.012	10000/4.5	1.3	1	20	SO-8	
CHM8809JPT	8809	30	+/-16	15500	0.0095	15000/4.5	3	1	24	SO-8	
CHM9436AJPT	9436A	30	+/-20	6200	0.052	5600/4.5	3	1	24	SO-8	
CHM4412JPT	4412	30	+/-20	7000	0.042	3500/4.5	3	1	30	SO-8	
CHM9410JPT	9410	30	+/-20	7000	0.05	3500/4.5	3	1	24	SO-8	
CHM3252JPT	3252	30	+/-20	7500	0.04	3500/4.5	3	1	30	SO-8	
CHM3172JPT	3172	30	+/-20	8900	0.028	5000/4.5	3	1	30	SO-8	
CHM4800AJPT	4416	30	+/-20	9000	0.028	7000/4.5	3	1	24	SO-8	
CHM4416JPT	3120	30	+/-20	9000	0.028	7300/4.5	3	1	30	SO-8	
CHM8410JPT	8410A	30	+/-20	10000	0.02	9000/4.5	3	1	24	SO-8	
CHM3120JPT	3120	30	+/-20	10000	0.022	4500/4.5	3	1	30	SO-8	
CHM4410AJPT	4410A	30	+/-20	10000	0.02	5000/4.5	3	1	30	SO-8	
CHM3082JPT	3082	30	+/-20	12000	0.015	10000/4.5	3	1	24	SO-8	
CHM4892JPT	4892	30	+/-20	12000	0.017	10000/4.5	2.5	1	24	SO-8	
CHM4410BJPT	4410B	30	+/-20	12500	0.014	5000/4.5	3	1	30	SO-8	
CHM8811JPT	8811	30	+/-20	12500	0.0135	10000/4.5	3	1	24	SO-8	
CHM3060JPT	3060	30	+/-20	14000	0.0115	14000/4.5	3	1	30	SO-8	
CHM4282JPT	4282	40	+/-20	6600	0.048	5300/4.5	3	1	40	SO-8	
CHM6426JPT	6426	60	+/-20	4700	0.085	3900/4.5	3	1	60	SO-8	
CHM4426JPT	4426	60	+/-20	5000	0.075	4500/4.5	3	1	48	SO-8	
CHM6336JPT	6336	60	+/-20	5800	0.055	4700/4.5	3	1	60	SO-8	
CHM4450JPT	4450	60	+/-20	7500	0.024	7500/10	4	1	60	SO-8	
DUAL N CHANNEL ENHANCEMENT MOS FET											
CHM8206JPT	8206	20	+/-12	6000	0.020	6000/4.5	1.5	1	20	SO-8	
CHM9926AJPT	9926A	20	+/-12	6000	0.020	6000/4.5	1.5	1	20	SO-8	
CHM9935AJPT	9935A	20	+/-12	6000	0.042	4300/4.5	1.5	1	20	SO-8	
CHM2108JPT	2108	20	+/-12	9500	0.014	10000/4.5	1.3	1	20	SO-8	
CHM9956AJPT	9956A	30	+/-20	3700	0.110	1000/4.5	3	1	24	SO-8	
CHM7101JPT	7101	30	+/-20	4000	0.100	1000/4.5	3	1	30	SO-8	
CHM9936AJPT	9936A	30	+/-20	5400	0.055	3200/4.5	3	1	24	SO-8	
CHM4936JPT	4936	30	+/-20	5800	0.055	4700/4.5	3	1	30	SO-8	
CHM3258JPT	3258	30	+/-20	7000	0.040	3500/4.5	3	1	30	SO-8	
CHM8912JPT	8912	30	+/-20	7000	0.045	3500/4.5	3	1	30	SO-8	
CHM4804AJPT	4804A	30	+/-20	7500	0.030	5000/4.5	3	1	30	SO-8	
CHM3178JPT	3178	30	+/-20	7800	0.030	5000/4.5	3	1	30	SO-8	
CHM4804JPT	4804	30	+/-20	7900	0.030	5000/4.5	3	1	30	SO-8	
CHM3128JPT	3128	30	+/-20	9000	0.023	7200/4.5	3	1	30	SO-8	
CHM4808JPT	4808	30	+/-20	9000	0.020	10000/4.5	2.5	1	24	SO-8	
CHM4308JPT	4308	40	+/-20	5800	0.050	5300/4.5	3	1	40	SO-8	
CHM4228JPT	4228	40	+/-20	6300	0.045	5000/4.5	3	1	40	SO-8	
CHM6428JPT	6428	60	+/-20	4100	0.086	3500/4.5	3	1	60	SO-8	
CHM4946JPT	4946	60	+/-20	4500	0.075	3900/4.5	3	2	60	SO-8	
CHM6338JPT	6338	60	+/-20	5200	0.055	4700/4.5	3	1	60	SO-8	

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SURFACE MOUNT DEVICES FOR HYBRID APPLICATIONS

PLASTIC MATERIAL USED CARRIES UL 94V-0

TYPE	Marking	Drain to Source Voltage	Gate to Source Voltage	Drain Current	Static Drain to Source On Resistance		Max. Gate Threshold Voltage	Zero Gate Voltage Drain Current		Outline No.	Equivalent Circuit Diagram
		V_{DSS}	V_{GSS}	$I_{D(DC)}$	$R_{DS(ON)}$ @ I_D / V_{GS}		$V_{GS(th)}$	I_{DSS} @ V_{DS},V_{GS}=0V			
		V	V	mA	ohmS	mA / V	V	uA	V		
P CHANNEL ENHANCEMENT MOS FET											
CHT2301WPT	21	-20	+/-8	-2300	0.130	-2800/-4.5	-0.6(Min)	-1	-16	SC-70	
CHT84WPT	AW	-50	+/-20	-130	10	-100/-5	-2	-15	-50	SC-70	
CHM1013TPT	13	-20	+/-12	-450	0.520	-450/-4.5	-0.8	-1	-20	SC-75	
CHM2331PT	31	-20	+/-8	-4200	0.048	-3300/-4.5	-0.9	-1	-20	SC-59	
CHM2305PT	05	-30	+/-12	-4000	0.070	-3500/-4.5	-1.3	-1	-30	SC-59	
CHT2303PT	03	-30	+/-20	-1900	0.320	-1300/-4.5	-3	-1	-30	SOT-23/SC-59	
CHM2307PT	07	-30	+/-20	-3200	0.120	-2500/-4.5	-3	-1	-30	SC-59	
CHM2313PT	13	-30	+/-20	-3600	0.090	-2000/-4.5	-3	-1	-30	SC-59	
CHM2323PT	23	-30	+/-20	-4100	0.080	-3200/-4.5	-3	-1	-30	SC-59	
CHT84PT	ZT	-50	+/-20	-130	10	-100/-5	-2	-15	-50	SOT-23	
CHT2301PT	01	-20	+/-8	-2300	0.130	-2800/-4.5	-0.6(Min)	-1	-16	SOT-23/SC-59	
CHM2321PT	21	-20	+/-12	-3800	0.055	-2400/-4.5	-1.5	-1	-16	SOT-23/SC-59	
P CHANNEL ENHANCEMENT MOS FET											
CHM3413SPT	13	-20	+/-12	-3500	0.095	-3400/-4.5	-0.8	-1	-20	SC-88	
CHM2313QPT	13	-30	+/-20	-4600	0.090	-3600/-4.5	-3	-1	-30	SC-74	
DUAL P CHANNEL ENHANCEMENT MOS FET											
CHT84SPT	VS	-50	+/-20	-130	10	-100/-5	-2	-15	-50	SC-88	
P CHANNEL ENHANCEMENT MOS FET											
CHM6861XPT	6861	-60	+/-20	-2400	0.180	-1900/-4.5	-3	-1	-60	SOT-89	
CHM9435AZPT	9435A	-30	+/-20	-5300	0.120	-4200/-4.5	-3	-1	-24	SOT223	
CHM4435AZPT	4435A	-30	+/-20	-8800	0.035	-5000/-4.5	-3	-1	-24	SOT223	
CHM4401ZPT	4401	-40	+/-20	-5600	0.085	-2900/-4.5	-3	-1	-40	SOT223	
CHM4301ZPT	4301	-40	+/-20	-6300	0.068	-3000/-4.5	-3	-1	-32	SOT223	
CHM6861ZPT	6861	-60	+/-20	-3500	0.170	-2800/-4.5	-3	-1	-60	SOT-223	
CHM9407AZPT	9407A	-60	+/-20	-3700	0.150	-3100/-4.5	-3	-1	-48	SOT223	
P CHANNEL ENHANCEMENT MOS FET											
CHM5P03PAPT	-	-30	+/-20	-15000	0.120	-4500/-4.5	-3	-1	-30	TO-252A	
CHM3423PAPT	-	-30	+/-20	-18000	0.080	-5000/-4.5	-3	-1	-24	TO-252A	
CHM4531PAPT	-	-30	+/-20	-25000	0.057	-2000/-4.5	-3	-1	-24	TO-252A	
CHM3301PAPT	-	-30	+/-20	-28000	0.050	-2000/-4.5	-3	-1	-30	TO-252A	
CHM4311PAPT	-	-30	+/-20	-33000	0.030	-9000/-4.5	-3	-1	-30	TO-252A	
CHM4301PAPT	-	-40	+/-20	-20000	0.065	-8000/-4.5	-3	-1	-32	TO-252A	
CHM20P06PAPT	-	-60	+/-20	-13000	0.150	-3900/-4.5	-3	-1	-48	TO-252A	
CHM6601PAPT	-	-60	+/-20	-16000	0.125	-6000/-4.5	-3	-1	-60	TO-252A	
CHM12P10PAPT	-	-100	+/-30	-9000	0.315	-4700/-10	-4	-1	-100	TO-252A	

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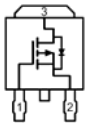
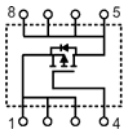
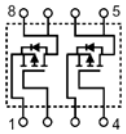
SURFACE MOUNT DEVICES FOR HYBRID APPLICATIONS

PLASTIC MATERIAL USED CARRIES UL 94V-0

TYPE	Marking	Drain to Source Voltage	Gate to Source Voltage	Drain Current	Static Drain to Source On Resistance		Max. Gate Threshold Voltage	Zero Gate Voltage Drain Current		Outline No.	Equivalent Circuit Diagram
		V_{DSS}	V_{GSS}	$I_{D(DC)}$	$R_{DS(ON)}$ @ I_D / V_{GS}		$V_{GS(th)}$	I_{DSS} @ V_{DS},V_{GS}=0V			
		V	V	mA	ohmS	mA / V	V	uA	V		
P CHANNEL ENHANCEMENT MOS FET											
CHM05P03NPT	-	-30	+/-20	-18000	0.120	-2000/-4.5	-3	-1	-30	D2PAK	
CHM35P03NPT	-	-30	+/-20	-35000	0.057	-16000/-5	-3	-1	-24	D2PAK	
CHM20P06NPT	-	-60	+/-20	-15000	0.150	-7000/-4.5	-3	-1	48	D2PAK	
CHM12P10NPT	-	-100	+/-30	-11000	0.315	-5750/-10	-4	-1	-100	D2PAK	
CHM8433JPT	8433	-20	+/-8	-5000	0.042	-5200/-4.5	-1.5	-1	-16	SO-8	
CHM9424JPT	9424	-20	+/-8	-7700	0.025	-7700/-4.5	-1	-1	-12	SO-8	
CHM9433JPT	9433	-20	+/-12	-5400	0.065	-5200/-4.5	-0.6(Min)	-1	-16	SO-8	
CHM2401JPT	2401	-20	+/-12	-6000	0.044	-4000/-4.5	-1	-1	-20	SO-8	
CHM9535JPT	9535	-30	+/-20	-5000	0.095	-4000/-4.5	-3	-1	-30	SO-8	
CHM9435AJPT	9435A	-30	+/-20	-5300	0.09	-4200/-4.5	-3	-1	-24	SO-8	
CHM4431JPT	4431	-30	+/-20	-5800	0.07	-2000/-4.5	-3	-1	-30	SO-8	
CHM4531JPT	4531	-30	+/-20	-6500	0.055	-2000/-4.5	-3	-1	-24	SO-8	
CHM3301JPT	3301	-30	+/-20	-7000	0.05	-4000/-4.5	-3	-1	-30	SO-8	
CHM4311JPT	4311	-30	+/-20	-7800	0.035	-5800/-4.5	-3	-1	-24	SO-8	
CHM8435AJPT	8435A	-30	+/-20	-7900	0.04	-5800/-4.5	-3	-1	-24	SO-8	
CHM4435AJPT	4435A	-30	+/-20	-8000	0.033	-5000/-4.5	-3	-1	-24	SO-8	
CHM6861JPT	6861	-60	+/-20	-3500	0.169	-3100/-4.5	-3	-1	-60	SO-8	
CHM9407AJPT	9407A	-60	+/-20	-3700	0.15	-3100/-4.5	-3	-1	-48	SO-8	
CHM6601JPT	6601	-60	+/-20	-4300	0.125	-3400/-4.5	-3	-1	-60	SO-8	
DUAL P CHANNEL ENHANCEMENT MOS FET											
CHM8933AJPT	8933A	-20	+/-10	-4600	0.058	-2200/-4.5	-0.5(Min)	-1	-16	SO-8	
CHM2407JPT	2407	-20	+/-12	-5300	0.045	-5300/-4.5	-1	-1	-20	SO-8	
CHM9953AJPT	9953A	-30	+/-20	-3500	0.130	-500/-4.5	-3	-1	-24	SO-8	
CHM4955JPT	4955	-30	+/-20	-4500	0.085	-3500/-4.5	-3	-1	-30	SO-8	
CHM4953JPT	4953	-30	+/-20	-4900	0.095	-3600/-4.5	-3	-1	-30	SO-8	
CHM4432JPT	4432	-30	+/-20	-5500	0.070	-2000/-4.5	-3	-1	-30	SO-8	
CHM8531JPT	8531	-30	+/-20	-5900	0.060	-2000/-4.5	-3	-1	-24	SO-8	
CHM8311JPT	8311	-30	+/-20	-7900	0.033	-5800/-4.5	-3	-1	-24	SO-8	
CHM4948JPT	4948	-60	+/-20	-3100	0.150	-2800/-4.5	-3	-2	-60	SO-8	
CHM6607JPT	6607	-60	+/-20	-3800	0.125	-3000/-4.5	-3	-1	-60	SO-8	

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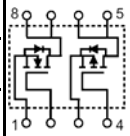
SURFACE MOUNT DEVICES FOR HYBRID APPLICATIONS

PLASTIC MATERIAL USED CARRIES UL 94V-0

TYPE	Marking	Drain to Source Voltage	Gate to Source Voltage	Drain Current	Static Drain to Source On Resistance		Max. Gate Threshold Voltage	Zero Gate Voltage Drain Current		Outline No.	Equivalent Circuit Diagram
		V_{DSS}	V_{GSS}	$I_{D(DC)}$	$R_{DS(ON)}$ @ I_D / V_{GS}		$V_{GS(th)}$	I_{DSS} @ V_{DS},V_{GS}=0V			
		V	V	mA	ohmS	mA / V	V	uA	V		

COMPLEMENTRY N & P CHANNEL ENHANCEMENT MOS FET

CHM2030JPT	2303	20	+/-12	6000	0.030	6000/4.5	1	1	20	SO-8	
		-20	+/-12	-4300	0.090	-2200/-4.5	-0.6(Min)	-1	-16		
CHM4501JPT	4501	20	+/-12	8300	0.018	8300/4.5	1.5	1	20	SO-8	
		-20	+/-12	-5000	0.042	-5000/-4.5	-1.5	-1	-20		
CHM8958JPT	8958	30	+/-20	7000	0.040	6000/4.5	3	1	24	SO-8	
		-30	+/-20	-5200	0.080	-4000/-4.5	-3	-1	-24		
CHM8968JPT	8968	30	+/-20	7000	0.040	6000/4.5	3	1	24	SO-8	
		-30	+/-20	-6200	0.052	-4000/-4.5	-3	-1	-24		
CHM11C2JPT	11C2	30	+/-20	7000	0.042	3500/4.5	3	1	30	SO-8	
		-20	+/-8	-4300	0.090	-2200/-4.5	-1.5	-1	-16		
CHM4600JPT	4600	30	+/-20	7300	0.032	6300/4.5	3	1	30	SO-8	
		-30	+/-20	-4600	0.090	-4000/-4.5	-3	-1	-30		
CHM8401JPT	8401	30	+/-20	7500	0.030	7400/4.5	3	1	30	SO-8	
		-30	+/-20	-5000	0.075	-3400/-4.5	-3	-1	-30		
CHM8938JPT	8938	30	+/-20	7000	0.038	6000/4.5	3	1	24	SO-8	
		-30	+/-20	-6000	0.057	-4000/-4.5	-3	-1	-24		
CHM9939AJPT	9939A	30	+/-20	7000	0.042	3500/4.5	3	1	30	SO-8	
		-30	+/-20	-3500	0.160	-2000/-4.5	-3	-1	-24		
CHM4539JPT	4539	30	+/-20	5800	0.055	4700/4.5	3	1	30	SO-8	
		-30	+/-20	-4900	0.095	-3600/-4.5	-3	-1	-30		
CHM4532JPT	4532	30	+/-20	4700	0.085	3700/4.5	3	1	30	SO-8	
		-30	+/-20	-4500	0.135	-3600/-4.5	-3	-1	-30		
CHM9952AJPT	9952A	30	+/-20	3700	0.110	500/4.5	3	1	24	SO-8	
		-30	+/-20	-2900	0.150	-500/-4.5	-3	-1	-24		
CHM4269JPT	4269	40	+/-20	6100	0.046	5000/4.5	3	1	40	SO-8	
		-40	+/-20	-5200	0.065	-2000/-4.5	-3	-1	-40		
CHM4559JPT	4559	60	+/-20	4500	0.075	4000/4.5	3	1	48	SO-8	
		-60	+/-20	-3500	0.160	-3100/-4.5	-3	-1	-48		
CHM7350JPT	7350	100	+/-20	2600	0.190	2100/10	4	1	100	SO-8	
		-100	+/-20	-2000	0.320	-1500/-10	-4	-1	-100		

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