## HAMAMATSU

**TECHNICAL DATA** 

DUAL CdS OUTPUT TYPE PHOTOCOUPLERS P873-G35-911, P873-13

## LED input, Dual CdS cell output, Cylindrical package

The P873-G35-911 and P873-13 are photocouplers which use a visible LED on the input side and a dual CdS cell on the output side. The P873-G35-911 is an electrically-connected dual CdS cell, while the P873-13 employs independent dual CdS cells. Both feature low harmonic distortion, making them suitable for use in audio instruments and electronic musical instruments.



- Dual CdS cell output
- Low harmonic distortion
- Cylindrical package

#### **APPLICATIONS**

- Audio instruments
- Electronic musical instruments
- Medical electronic equipment

#### MAXIMUM RATINGS (Ta = 25°C)

	Parameters	Symbols	Ratings	Unit	
Input	Forward Current	1 <sub>F</sub>	25	mA	
	Reverse Voltage	V <sub>R</sub>	4	Vdc	
	Power Dissipation	Р	100	mW	
Output	Supply Volage	V <sub>s</sub>	100	Vdc	
	Power Dissipation	Po	30	mW	
Operating Temperature		T <sub>opr</sub>	-30 ~ +50	°C	
Storage Temperature		T <sub>stg</sub>	-30~+50	°C	

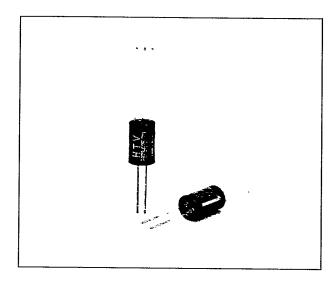
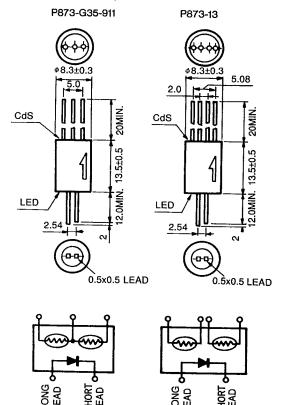


Figure 1: Dimensional Output and Pin Connection (Unit:mm)



### **DUAL CdS OUTPUT PHOTOCOUPLERS P873-G35-911, P873-13**

#### **ELECTRICAL CHARACTERISTICS (Ta = 25°C, One Element)**

Parameters		Symbols	Conditions	P873-G35-911		P873-13				
				Min.	Тур.	Max.	Min.	Typ.	Max.	Unit
Input	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =20mA		2.1	-		2.1		Vdc
	Recommended Forward Current	l <sub>F</sub>		-	20			20		mA
Output	ON Resistance	R <sub>on</sub>	I <sub>F</sub> =20mA	0.3	_	1.0	_	-	10	kΩ
	OFF Resistance	R <sub>OFF</sub>	10 seconds after I <sub>F</sub> goes OFF	1	_		10	_	_	МΩ
Transfer Characteristics	Input-Output Isolation Voltage	V <sub>iso</sub>	RH40 ~ 60%, 1 minute	5000	-	-	5000	-	_	Vrms
	Rise Time (1)	t <sub>r</sub>	1 00 1	_	5.0	20	_	4.0	20	ms
	Fall Time (1)	t <sub>f</sub>	I <sub>F</sub> =20mA	_	5.0	20	_	6.0	20	ms

(1) Response Time Measuring Circuit

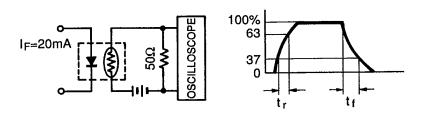


Figure 2: LED Allowable Forward Current vs.

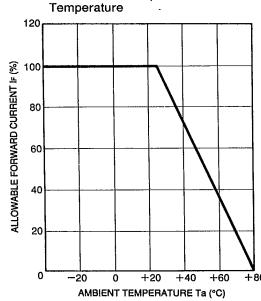
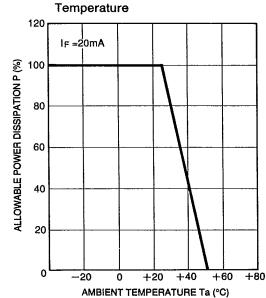


Figure 3: CdS Cell Allowable Power Dissipation vs.



# IAMA

Figure 4: LED Forward Current vs. Forward Voltage

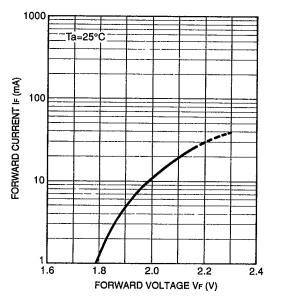


Figure 5: Output Resistance vs. Forward Current

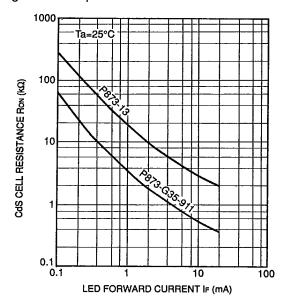


Figure 6: Rise/Fall Time vs. Load Resistance (G873-G35-911)

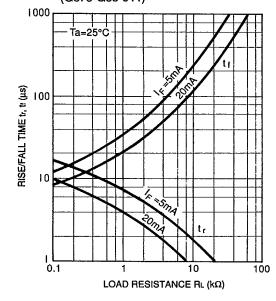


Figure 7: Output Resistance vs. Temperature (G873-G35-911)

